



# TGF2979-SM

25 W, 32 V, DC – 12 GHz, GaN RF Transistor

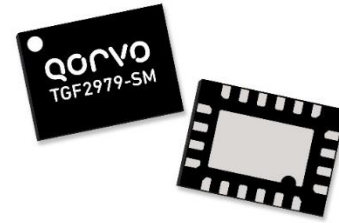
## Product Overview

The Qorvo TGF2979-SM is a 25 W (P3dB) discrete GaN on SiC HEMT which operates from DC to 12 GHz. The device is constructed with Qorvo’s proven QGaN25 process, which features advanced field plate techniques to optimize power and efficiency at high drain bias operating conditions. This optimization can potentially lower system costs in terms of fewer amplifier line-ups and lower thermal management costs.

The device is housed in an industry-standard 3 x 4 mm surface mount QFN package.

Lead-free and ROHS compliant

Evaluation boards are available upon request

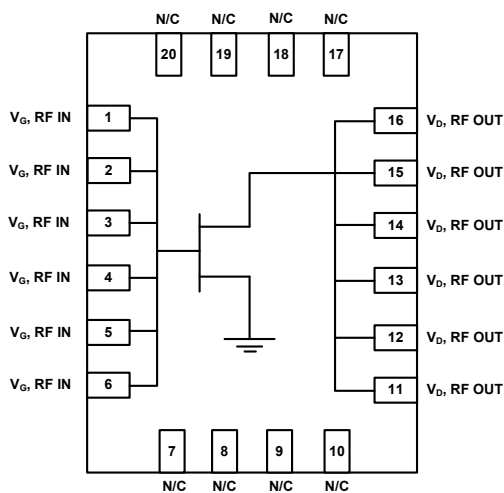


20 Pad 3 x 4 mm Package

## Key Features

- Frequency: DC to 12 GHz
  - Output Power (P3dB)<sup>1</sup>: 22 W
  - Linear Gain<sup>1</sup>: 11 dB
  - Typical PAE (3dB)<sup>1</sup>: 45%
  - Operating Voltage: 32 V
  - Low thermal resistance package
  - CW and Pulse capable
  - 3 x 4 mm package
- Note: @ 9.4 GHz

## Functional Block Diagram



## Applications

- Military radar
- Commercial radar
  - Avionics
  - Marine
  - Weather

## Ordering Information

Part No.	Description
1127378	TGF2979-SM, 25 pcs.

## Absolute Maximum Ratings

Parameter	Value	Units
Breakdown Voltage ( $V_{BDG}$ )	100	V
Gate Voltage Range ( $V_G$ )	-7 to +2	V
Drain Current ( $I_D$ )	3.6	A
Gate Current ( $I_G$ )	-7.5 to 12.6	mA
Power Dissipation, CW ( $P_D$ )	See page 4.	W
RF Input Power, CW, $T=25^\circ\text{C}$ ( $P_{IN}$ )	37.8	dBm
Storage Temperature	-40 to 150	$^\circ\text{C}$

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability.

## Recommended Operating Conditions

Parameter	Min	Typ	Max	Units
Drain Voltage ( $V_D$ )		+32		V
Drain Quiescent Current ( $I_{DQ}$ )		150		mA
Gate Voltage ( $V_G$ ) <sup>1</sup>		-2.7		V

Electrical specifications are measured at specified test conditions. Specifications are not guaranteed over all recommended operating conditions.

Note:

- To be adjusted to desired  $I_{DQ}$

## Pulsed RF Characterization – Load Pull Performance

Test conditions unless otherwise noted:  $T = 25^\circ\text{C}$ , Pulse (20% Duty Cycle, 100  $\mu\text{s}$  Width).

Parameter	Typical Values						Units
	5	8	9	9.4	10	12	
Frequency, F							GHz
Drain Voltage <sup>1,2</sup> , $V_D$	32	32	32	32	32	32	V
Drain Bias Current <sup>1,2</sup> , $I_{DQ}$	150	150	150	150	150	150	mA
Output Power at 3dB compression <sup>1</sup> , $P_{3dB}$	44.4	43.8	43.2	43.5	43.7	43.4	dBm
Power Added Efficiency at 3dB compression <sup>2</sup> , $PAE_{3dB}$	56.0	51.0	49.2	44.9	40.8	33.00	%
Gain at 3dB compression <sup>1</sup> , $G_{3dB}$	11.5	9.7	8.8	7.9	7.1	6.2	dB

Notes:

- Power Tuned
- Efficiency Tuned

### Thermal and Reliability Information – CW<sup>(1)</sup>

Parameter	Simulation Conditions	Value	Units
Thermal Resistance, Peak IR Surface Temperature at Average Power ( $\theta_{JC}$ )	$P_{DISS} = 45.4 \text{ W}$ , $T_{baseplate} = 85 \text{ }^\circ\text{C}$	2.53	$^\circ\text{C/W}$
Channel Temperature ( $T_{CH}$ )		200	$^\circ\text{C}$
Thermal Resistance, Peak IR Surface Temperature at Average Power ( $\theta_{JC}$ )	$P_{DISS} = 37.8 \text{ W}$ , $T_{baseplate} = 85 \text{ }^\circ\text{C}$	2.43	$^\circ\text{C/W}$
Channel Temperature ( $T_{CH}$ )		177	$^\circ\text{C}$
Thermal Resistance, Peak IR Surface Temperature at Average Power ( $\theta_{JC}$ )	$P_{DISS} = 30.2 \text{ W}$ , $T_{baseplate} = 85 \text{ }^\circ\text{C}$	2.35	$^\circ\text{C/W}$
Channel Temperature ( $T_{CH}$ )		156	$^\circ\text{C}$
Thermal Resistance, Peak IR Surface Temperature at Average Power ( $\theta_{JC}$ )	$P_{DISS} = 22.7 \text{ W}$ , $T_{baseplate} = 85 \text{ }^\circ\text{C}$	2.28	$^\circ\text{C/W}$
Channel Temperature ( $T_{CH}$ )		137	$^\circ\text{C}$
Thermal Resistance, Peak IR Surface Temperature at Average Power ( $\theta_{JC}$ )	$P_{DISS} = 15.1 \text{ W}$ , $T_{baseplate} = 85 \text{ }^\circ\text{C}$	2.19	$^\circ\text{C/W}$
Channel Temperature ( $T_{CH}$ )		118	$^\circ\text{C}$
Thermal Resistance, Peak IR Surface Temperature at Average Power ( $\theta_{JC}$ )	$P_{DISS} = 7.6 \text{ W}$ , $T_{baseplate} = 85 \text{ }^\circ\text{C}$	1.96	$^\circ\text{C/W}$
Channel Temperature ( $T_{CH}$ )		100	$^\circ\text{C}$

Note:

1. Thermal resistance measured to bottom of package.
2. Refer to the following document: [GaN Device Channel Temperature, Thermal Resistance, and Reliability Estimates](#)

### Thermal and Reliability Information – Pulsed<sup>(1)</sup>

Parameter	Simulation Conditions	Value	Units
Thermal Resistance, Peak IR Surface Temperature at Average Power ( $\theta_{JC}$ )	$P_{DISS} = 45.4 \text{ W}$ , $T_{baseplate} = 85 \text{ }^\circ\text{C}$ Pulse Width = 500 $\mu\text{S}$	2.36	$^\circ\text{C/W}$
Channel Temperature ( $T_{CH}$ )		Duty Cycle = 10%	192
Thermal Resistance, Peak IR Surface Temperature at Average Power ( $\theta_{JC}$ )	$P_{DISS} = 37.8 \text{ W}$ , $T_{baseplate} = 85 \text{ }^\circ\text{C}$ Pulse Width = 500 $\mu\text{S}$	2.20	$^\circ\text{C/W}$
Channel Temperature ( $T_{CH}$ )		Duty Cycle = 10%	168
Thermal Resistance, Peak IR Surface Temperature at Average Power ( $\theta_{JC}$ )	$P_{DISS} = 45.4 \text{ W}$ , $T_{baseplate} = 85 \text{ }^\circ\text{C}$ Pulse Width = 100 $\mu\text{S}$	1.82	$^\circ\text{C/W}$
Channel Temperature ( $T_{CH}$ )		Duty Cycle = 10%	168
Thermal Resistance, Peak IR Surface Temperature at Average Power ( $\theta_{JC}$ )	$P_{DISS} = 37.8 \text{ W}$ , $T_{baseplate} = 85 \text{ }^\circ\text{C}$ Pulse Width = 100 $\mu\text{S}$	1.78	$^\circ\text{C/W}$
Channel Temperature ( $T_{CH}$ )		Duty Cycle = 10%	152

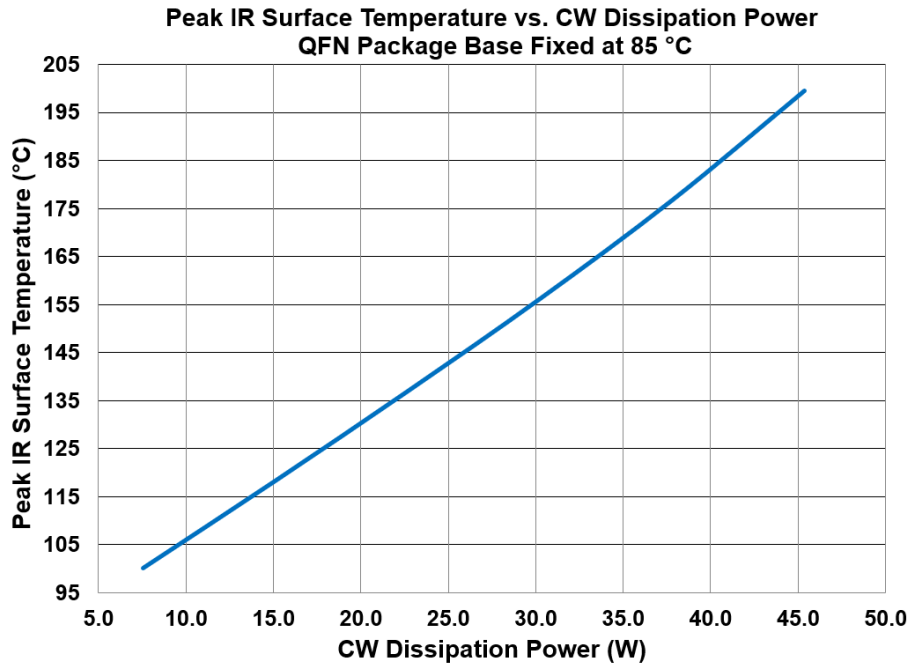
Note:

1. Thermal resistance measured to bottom of package.
2. Refer to the following document: [GaN Device Channel Temperature, Thermal Resistance, and Reliability Estimates](#)

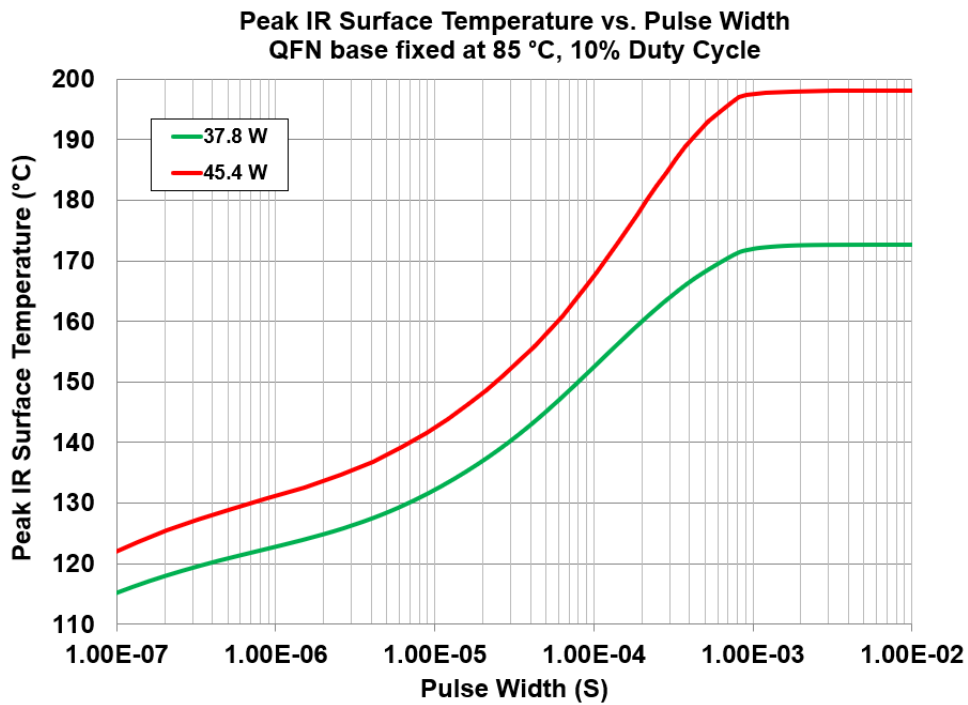
### Electrical Specifications

Parameter	Conditions	Min	Typ	Max	Units
Gate Leakage	$V_D = +10$ , $V_G = -3.7$	-8.25			mA

Maximum Channel Temperature, CW



Maximum Channel Temperature, Pulsed

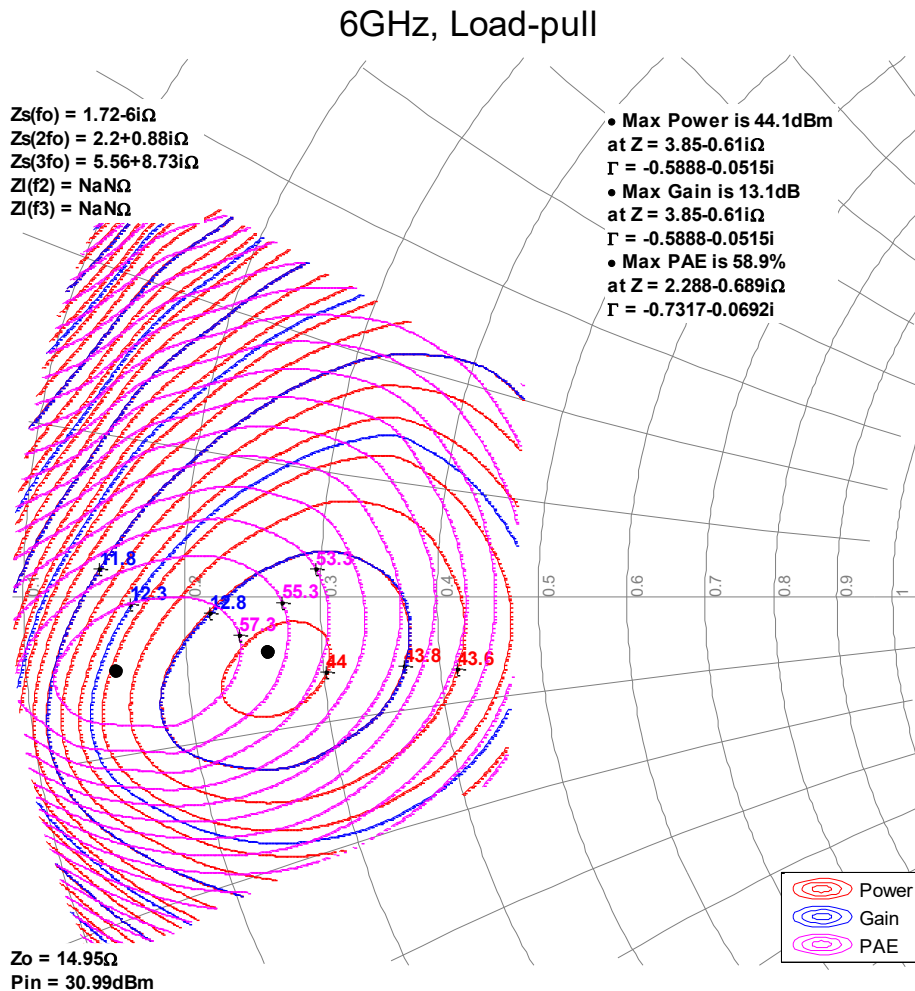


**Load Pull Smith Charts – Pulsed<sup>(1,2,3)</sup>**

RF performance that the device typically exhibits when placed in the specified impedance environment. The impedances are not the impedances of the device, they are the impedances presented to the device via an RF circuit or load-pull system. The impedances listed follow an optimized trajectory to maintain high power and high efficiency.

Notes:

1. 32 V, 150 mA, Pulsed signal with 100 uS pulse width and 10 % duty cycle. Performance is at indicated input power.
2. See page 13 for load pull and source pull reference planes. 15-Ω load pull TRL fixtures are built with 10-mil RO4350B material.
3. NaN means the impedances are either undefined or varying in load-pull system.



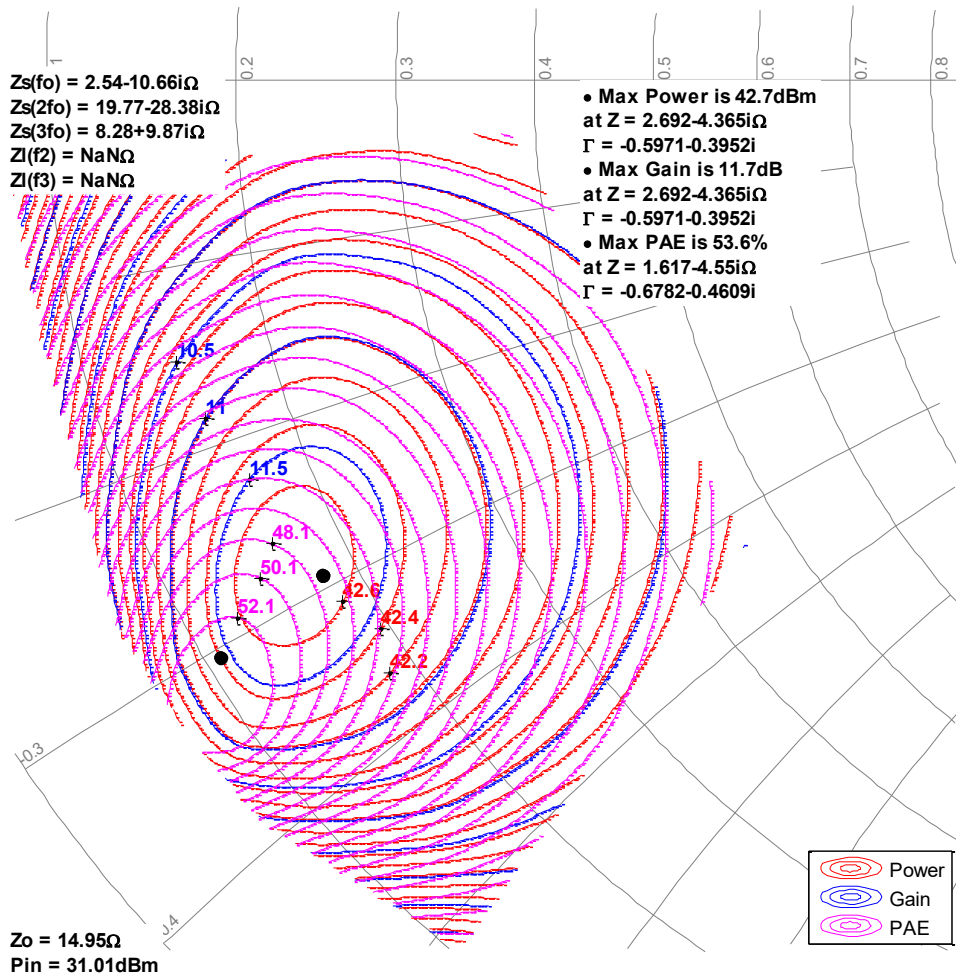
**Load Pull Smith Charts – Pulsed<sup>(1,2,3)</sup>**

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**8GHz, Load-pull**



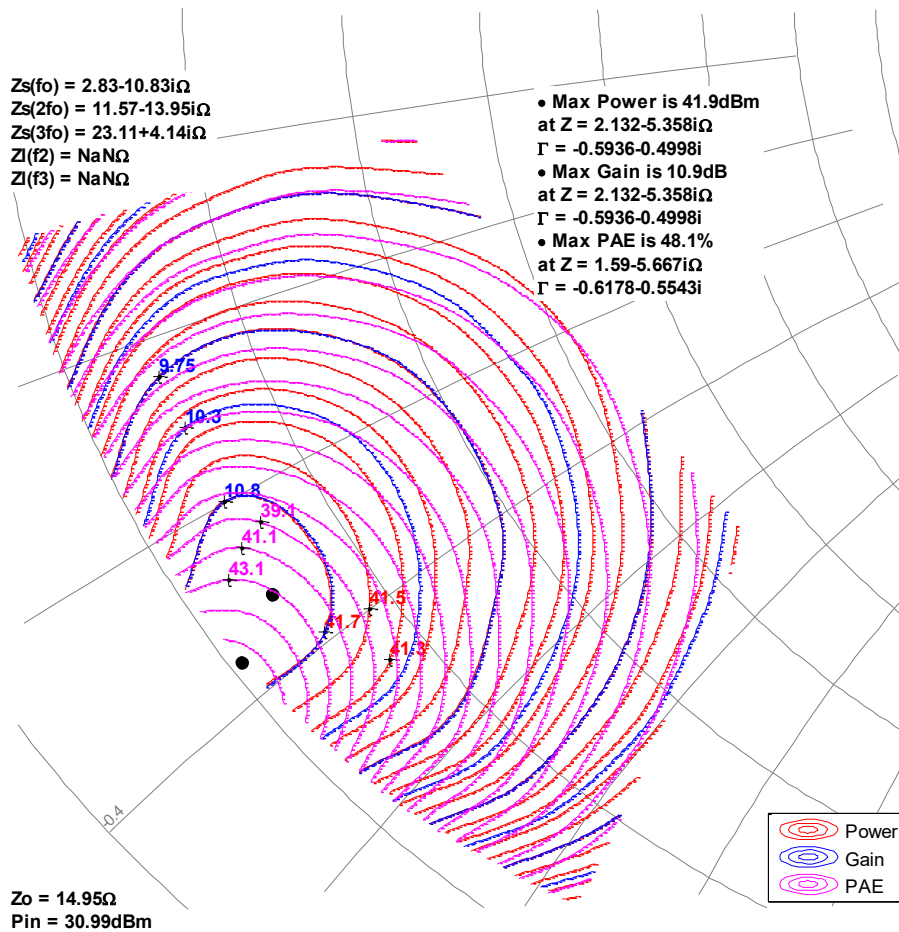
**Load Pull Smith Charts – Pulsed<sup>(1,2,3)</sup>**

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3. NaN means the impedances are either undefined or varying in load-pull system.

**9GHz, Load-pull**



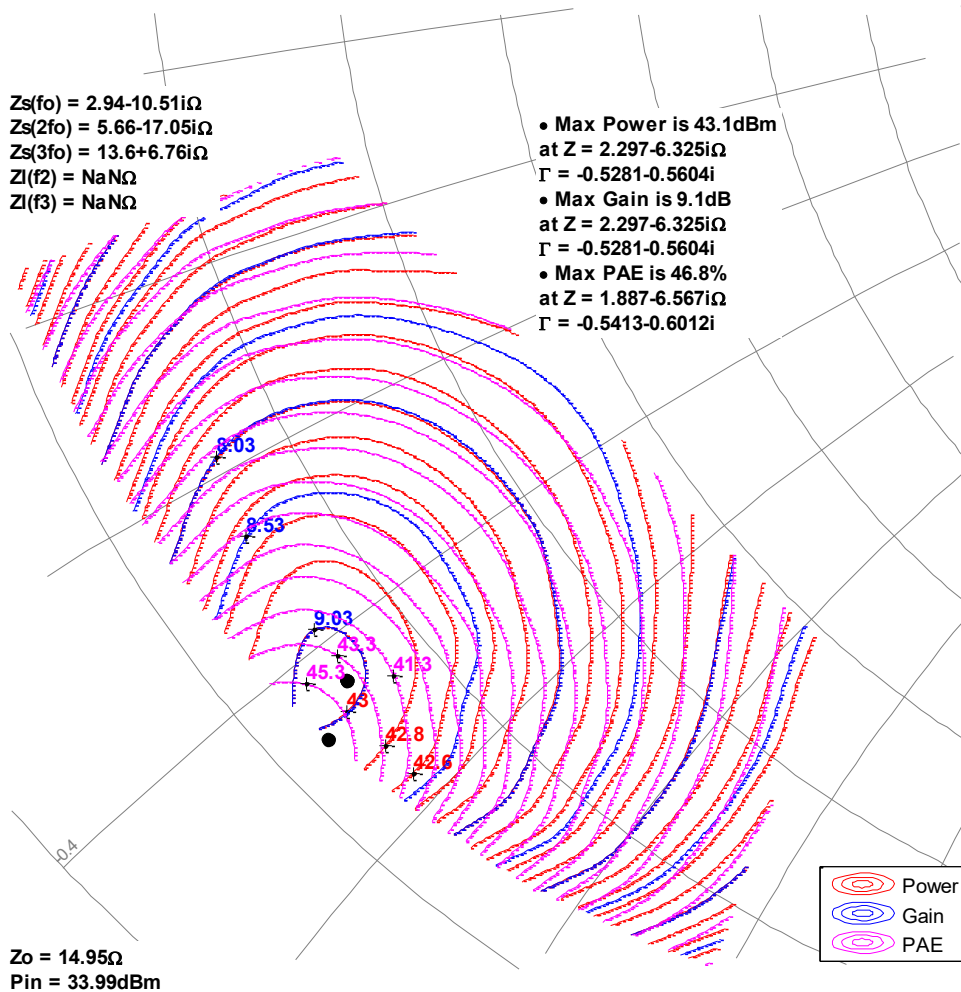
Load Pull Smith Charts – Pulsed<sup>(1,2,3)</sup>

RF performance that the device typically exhibits when placed in the specified impedance environment. The impedances are not the impedances of the device, they are the impedances presented to the device via an RF circuit or load-pull system. The impedances listed follow an optimized trajectory to maintain high power and high efficiency.

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2. See page 13 for load pull and source pull reference planes. 15-Ω load pull TRL fixtures are built with 10-mil RO4350B material.
3. NaN means the impedances are either undefined or varying in load-pull system.

9.4GHz, Load-pull



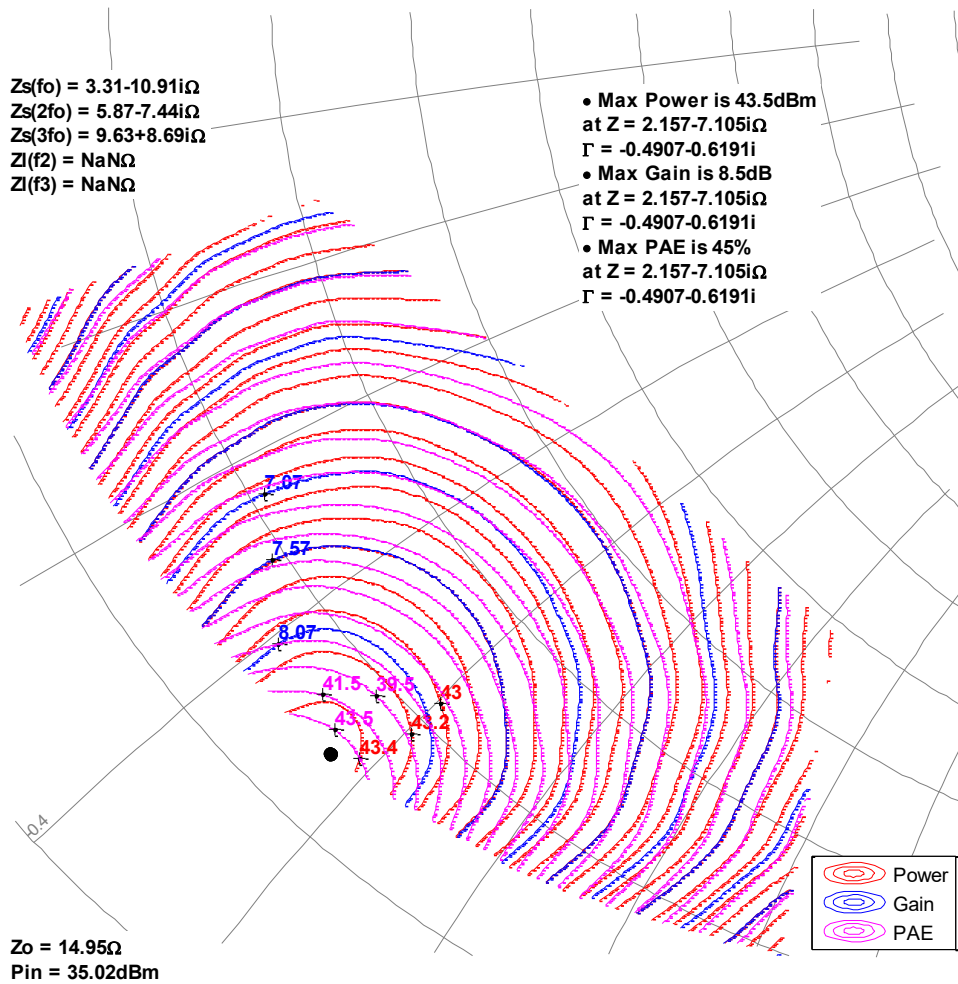
**Load Pull Smith Charts – Pulsed<sup>(1,2,3)</sup>**

RF performance that the device typically exhibits when placed in the specified impedance environment. The impedances are not the impedances of the device, they are the impedances presented to the device via an RF circuit or load-pull system. The impedances listed follow an optimized trajectory to maintain high power and high efficiency.

Notes:

1. 32 V, 150 mA, Pulsed signal with 100 uS pulse width and 10 % duty cycle. Performance is at indicated input power.
2. See page 13 for load pull and source pull reference planes. 15-Ω load pull TRL fixtures are built with 10-mil RO4350B material.
3. NaN means the impedances are either undefined or varying in load-pull system.

**10GHz, Load-pull**



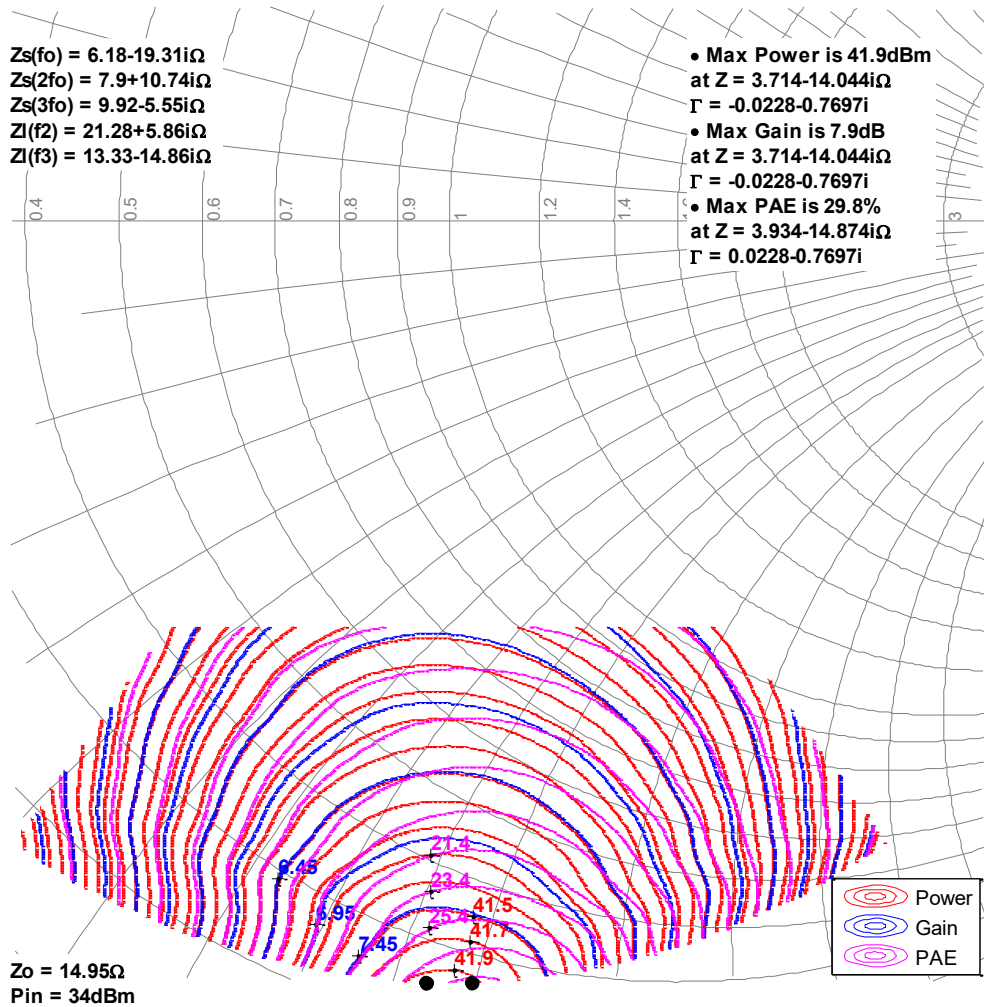
**Load Pull Smith Charts – Pulsed<sup>(1,2,3)</sup>**

RF performance that the device typically exhibits when placed in the specified impedance environment. The impedances are not the impedances of the device, they are the impedances presented to the device via an RF circuit or load-pull system. The impedances listed follow an optimized trajectory to maintain high power and high efficiency.

Notes:

1. 32 V, 150 mA, Pulsed signal with 100 uS pulse width and 10 % duty cycle. Performance is at indicated input power.
2. See page 13 for load pull and source pull reference planes. 15-Ω load pull TRL fixtures are built with 10-mil RO4350B material.
3. NaN means the impedances are either undefined or varying in load-pull system.

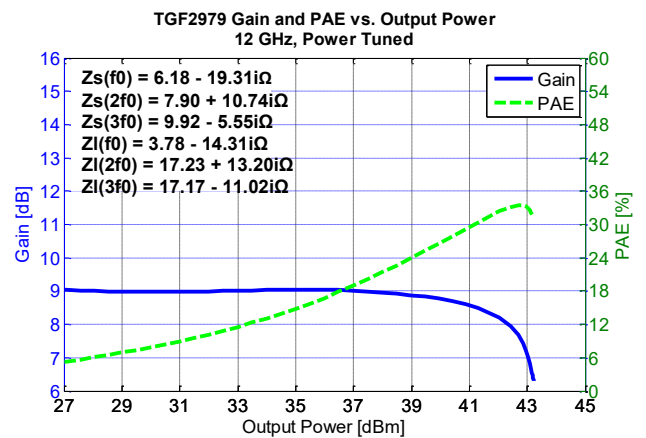
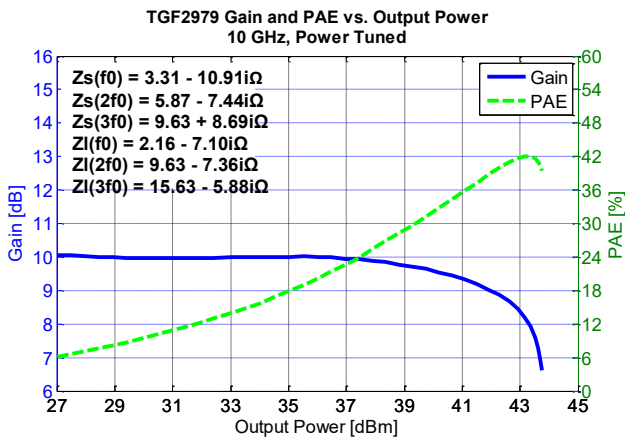
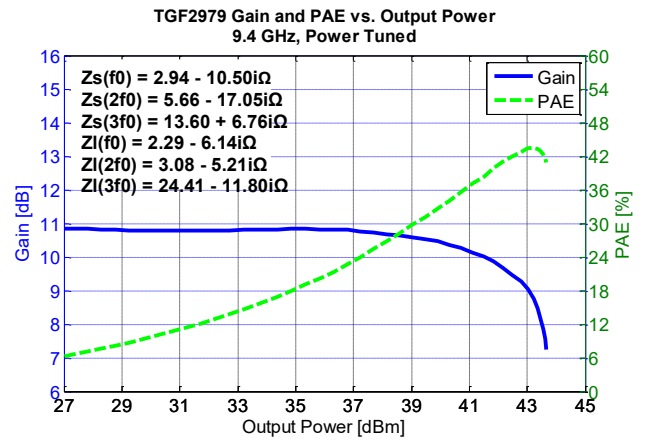
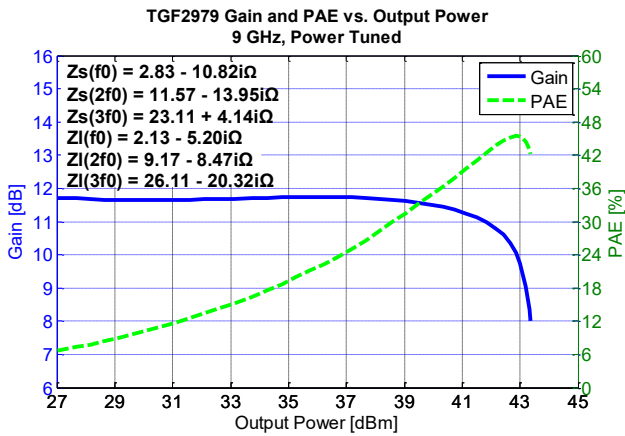
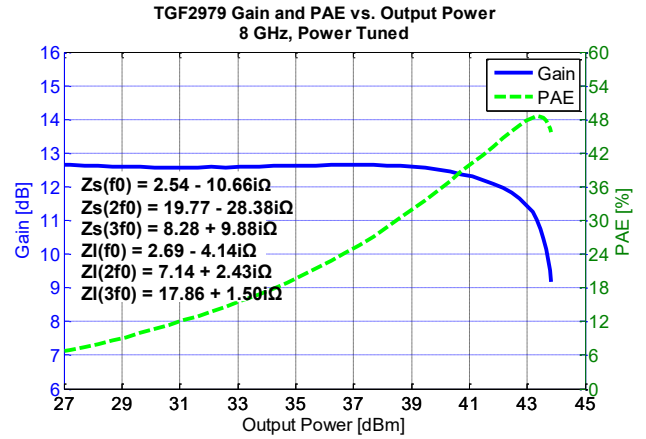
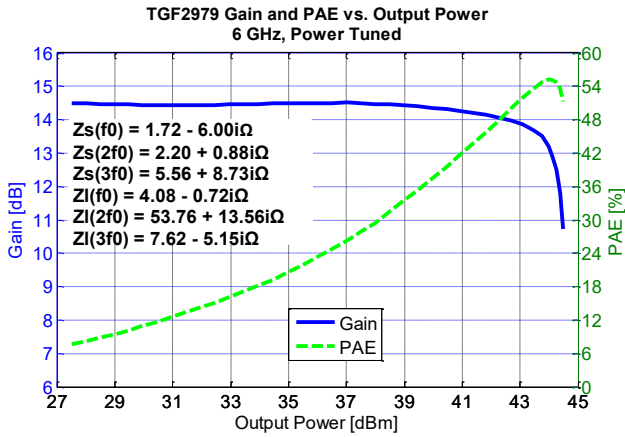
**12GHz, Load-pull**



Typical Pulsed Performance – Power Tuned<sup>(1,2)</sup>

Notes:

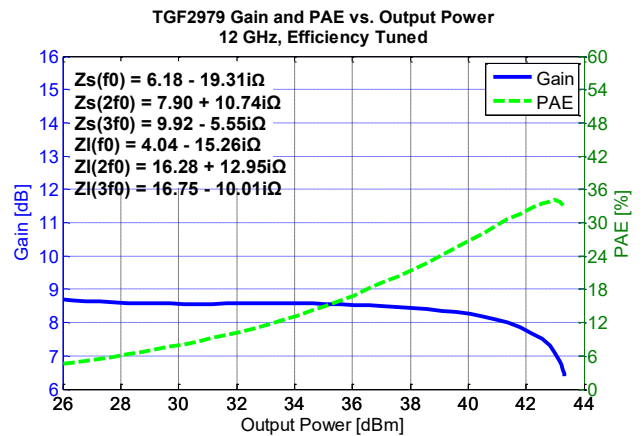
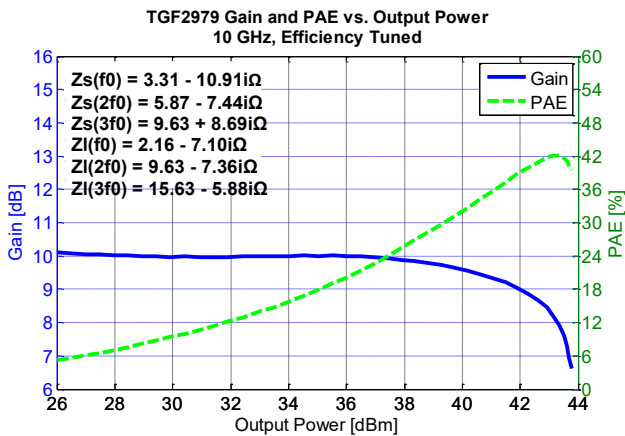
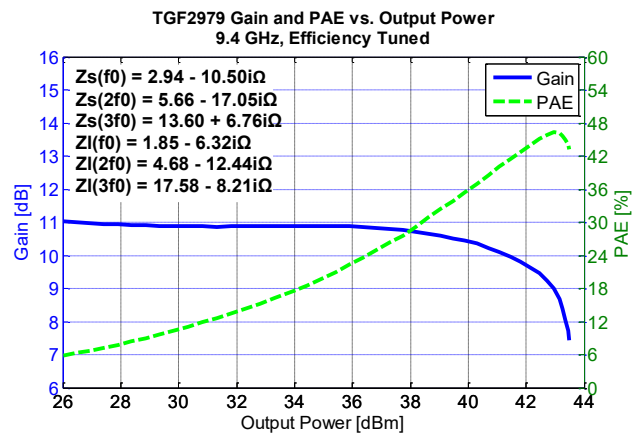
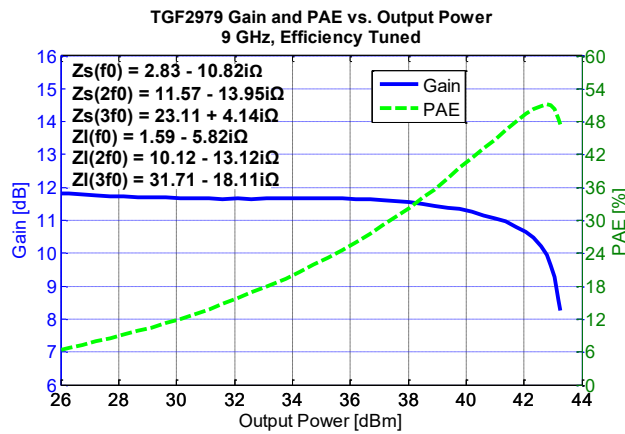
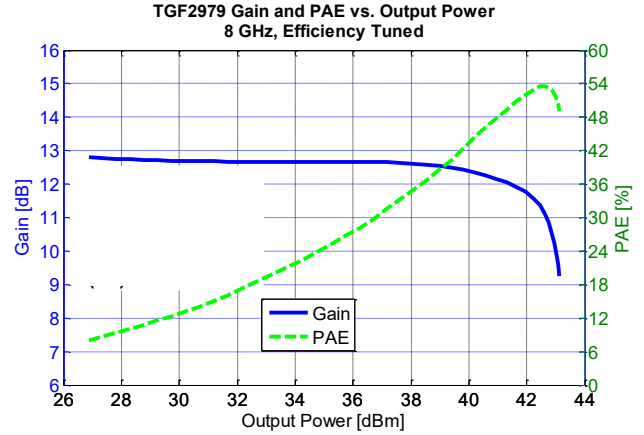
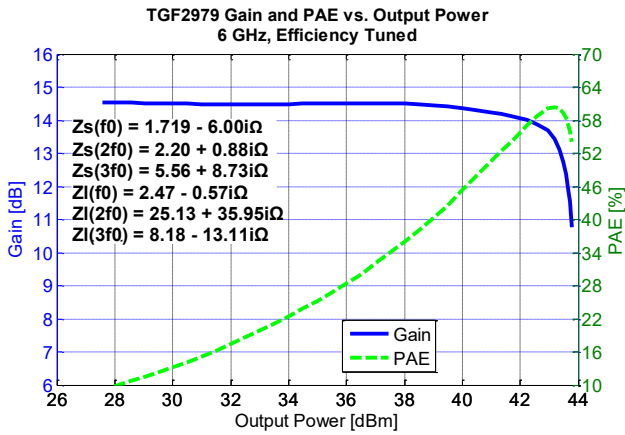
1. Pulsed signal with 100uS pulse width and 10% duty cycle
2. See page 13 for load pull and source pull reference planes where the performance was measured.



Typical Pulsed Performance – Efficiency Tuned<sup>(1,2)</sup>

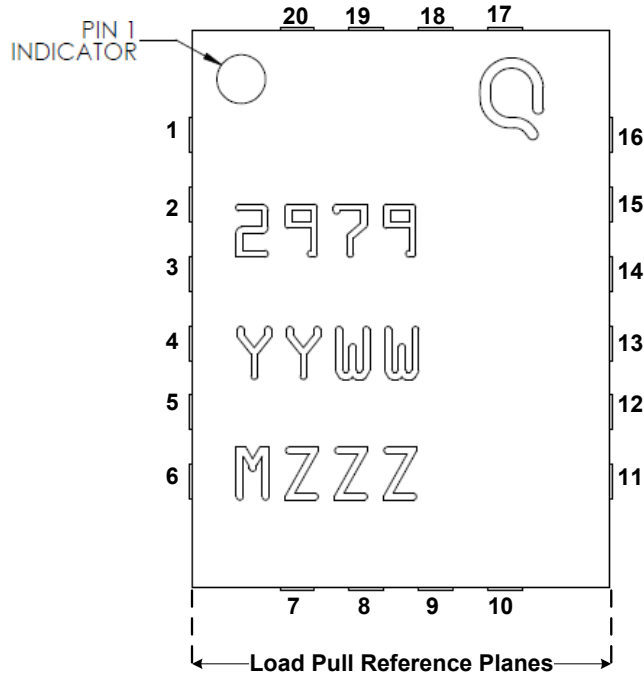
Notes:

1. Pulsed signal with 100uS pulse width and 10% duty cycle
2. See page 13 for load pull and source pull reference planes where the performance was measured.



## Pin Layout

Marking: Qorvo Logo  
 Part Number – TGF2979-SM (The TGF2979-SM will be marked with the “2979” designator)  
 Date Code – YYWW  
 Lot Code – MZZZ

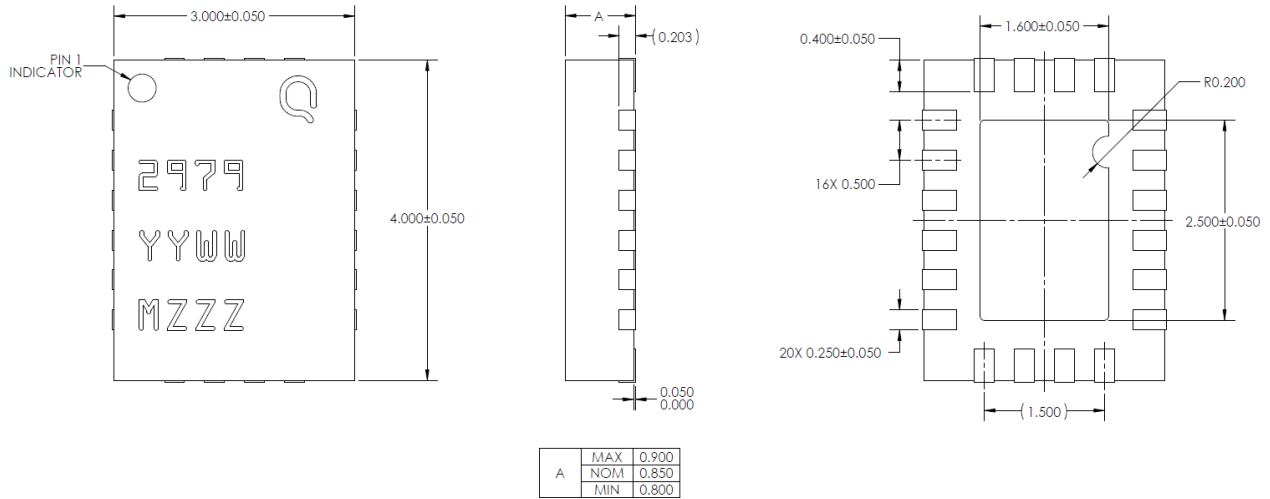


## Pin Description

Pin	Symbol	Description
11 – 16	$V_D$ /RF OUT	Drain voltage / RF Output to be matched to 50 ohms;
1 – 6	$V_G$ /RF IN	Gate voltage / RF Input to be matched to 50 ohms; see
7 – 10, 17 – 20	N/C	Not connected
Back side	Source	Source connected to ground

**Mechanical Information**

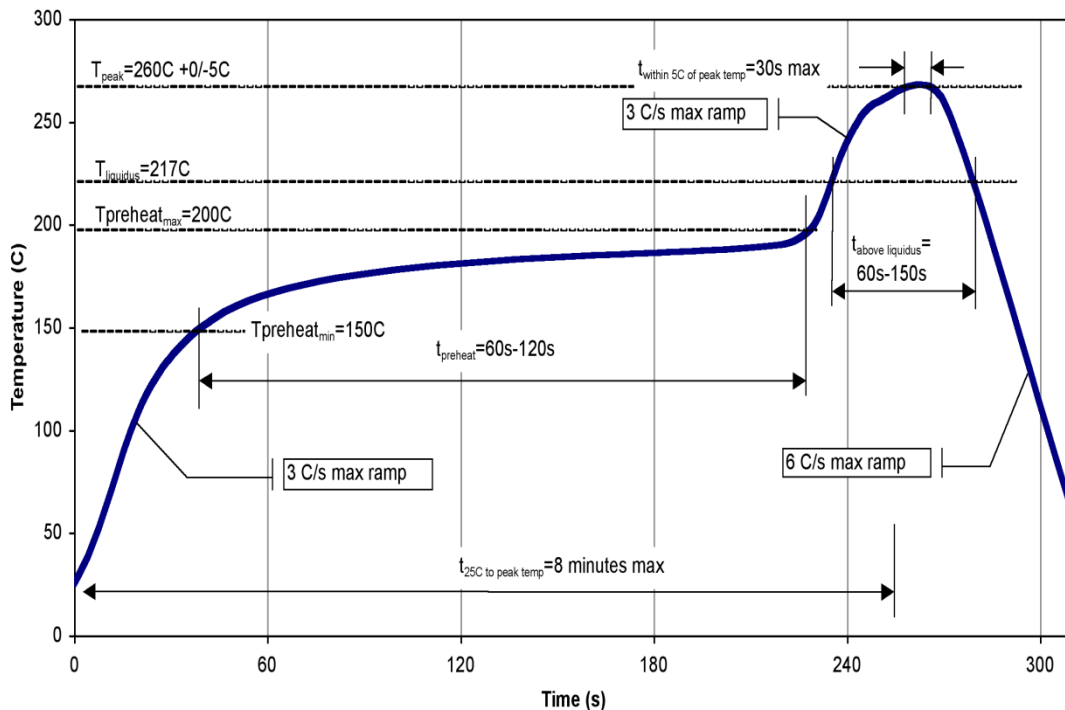
All dimensions are in millimeters.



**Note:**

1. Unless otherwise noted, all dimension tolerances are  $\pm 0.127$  mm.
2. This package is lead-free/RoHS-compliant. The plating material on the leads is NiAu. It is compatible with both lead-free (maximum 260 °C reflow temperature) and tin-lead (maximum 245°C reflow temperature) soldering processes.

**Recommended Solder Temperature Profile**



## Handling Precautions

Parameter	Rating	Standard
ESD – Human Body Model (HBM)	Class 1A	ANSI/ESDA/JEDEC JS-001
ESD – Charged Device Model (CDM)	Class C2A	ANSI/ESDA/JEDEC JS-002
MSL – Moisture Sensitivity Level	Level 3	IPC/JEDEC J-STD-020



Caution!  
 ESD-Sensitive Device

## Solderability

Compatible with lead-free (260°C max. reflow temp.) soldering processes.

## RoHS Compliance

This part is compliant with 2011/65/EU RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment) as amended by Directive 2015/863/EU.

This product also has the following attributes:

- Lead Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A (C<sub>15</sub>H<sub>12</sub>Br<sub>4</sub>O<sub>2</sub>) Free
- PFOS Free
- SVHC Free

## Contact Information

For the latest specifications, additional product information, worldwide sales and distribution locations:

**Web:** [www.qorvo.com](http://www.qorvo.com)

**Tel:** 1-844-890-8163

**Email:** [customer.support@qorvo.com](mailto:customer.support@qorvo.com)

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