

Product Overview

The QPD1034 is a 1700 W (P_{3dB}) fully matched GaN RF pallet which operates from 0.96 to 1.215 GHz on a 65 V supply rail. The pallet uses GaN on SiC HEMT technology and is ideally suitable for IFF, avionics and test instrumentation. The pallet can support pulsed operations.

Pallets are available upon request.



QPD1034 Pallet Performance

Freq.(GHz)	$P_{3dB}(W)$	$G_{3dB}(dB)$	$DE_{3dB}(\%)$
0.96	1858.6	15.9	64.7
1.05	1636.4	17.7	66.1
1.15	1734.2	16.4	61.6
1.215	1775.8	16.4	65.4

Signal Type: Pulse Width = 100us, Duty Cycle = 10%
 $V_D = 65V$, $I_{DQ} = 1500mA$
 At Bottom of Baseplate Temperature of 25°C

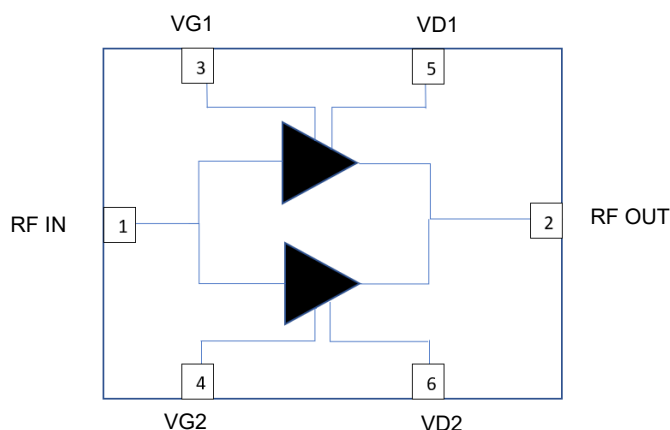
Key Features

- Operating Frequency: 0.96 to 1.215 GHz
- Operating Voltage: 65 V
- Linear Gain: 19.4 dB ⁽¹⁾
- Output Power (P_{3dB}): 1734 W ⁽¹⁾
- Drain Efficiency (P_{3dB}): 61.6% ⁽¹⁾
- Pulse Operations
- Pallet EVB Size: 4.250" x 2.235" x 0.638"

Notes:

(1) Pallet Performance @ 1.15 GHz

Functional Block Diagram



Applications

- DME Radar
- IFF Transponders
- Avionics

Ordering Information

Part No.	Description
QPD1034	0.96 – 1.215 GHz GaN RF Pallet

Absolute Maximum Ratings ^{1, 2, 3}

Parameter	Rating	Units
Breakdown Voltage, BV_{DG}	225	V
Gate Voltage Range, V_G	-7 to +2	V
Drain Current, $I_{D_{MAX}}$	142	A
Power Dissipation, Pulsed, P_{DISS}^2	1209	W
RF Input Power, Pulsed, P_{IN}^3	46.2	dBm
Storage Temperature	-65 to +150	°C

Notes:

1. Operation of this device outside the parameter ranges given above may cause permanent damage
2. Pulsed, 100us PW, 10% DC, Package base at 85 °C
3. Pulsed, 100us PW, 10% DC, T = 25 °C

Recommended Operating Conditions ^{1, 2, 3, 4}

Parameter	Min	Typ	Max	Units
Operating Temp. Range	-40	+25	+85	°C
Drain Voltage Range, V_D	–	+65	+70	V
Drain Bias Current, I_{DQ}		1.5		A
Drain Current, I_D^4	–	28	–	A
Gate Voltage, V_G^3	–	-2.8	–	V
Power Dissipation (P_D) ^{2,4}	–	–	685	W
Power Dissipation (P_D), CW ²	–	–	496	W

Notes:

1. Electrical performance is measured under conditions noted in the electrical specifications table. Specifications are not guaranteed over all recommended operating conditions
2. Package base at 85 °C
3. To be adjusted to desired I_{DQ}
4. Pulsed, 1000us PW, 20% DC

RF Characterization – 0.96 – 1.215 GHz Pallet Performance at 1.15 GHz ^{1, 2}

Parameter	Min	Typ	Max	Units
Linear Gain, G_{LIN}	–	19.4	–	dB
Output Power at 3dB compression point, P3dB	–	1734	–	W
Drain Efficiency at 3dB compression point, DE3dB	–	61.6	–	%
Gain at 3dB compression point, G3dB	–	16.4	–	dB
Gate Leakage $V_D = +10$ V, $V_G = -3.7$ V	-40	–	–	mA

Notes:

1. $V_D = 65$ V, $I_{DQ} = 1.5$ A (combined), Temp = +25 °C, Pulse Width = 100 μ s, Duty Cycle = 10%
2. Gate Leakage per path

RF Characterization – Impedance Mismatch at 1.0 GHz ^{1, 2, 3}

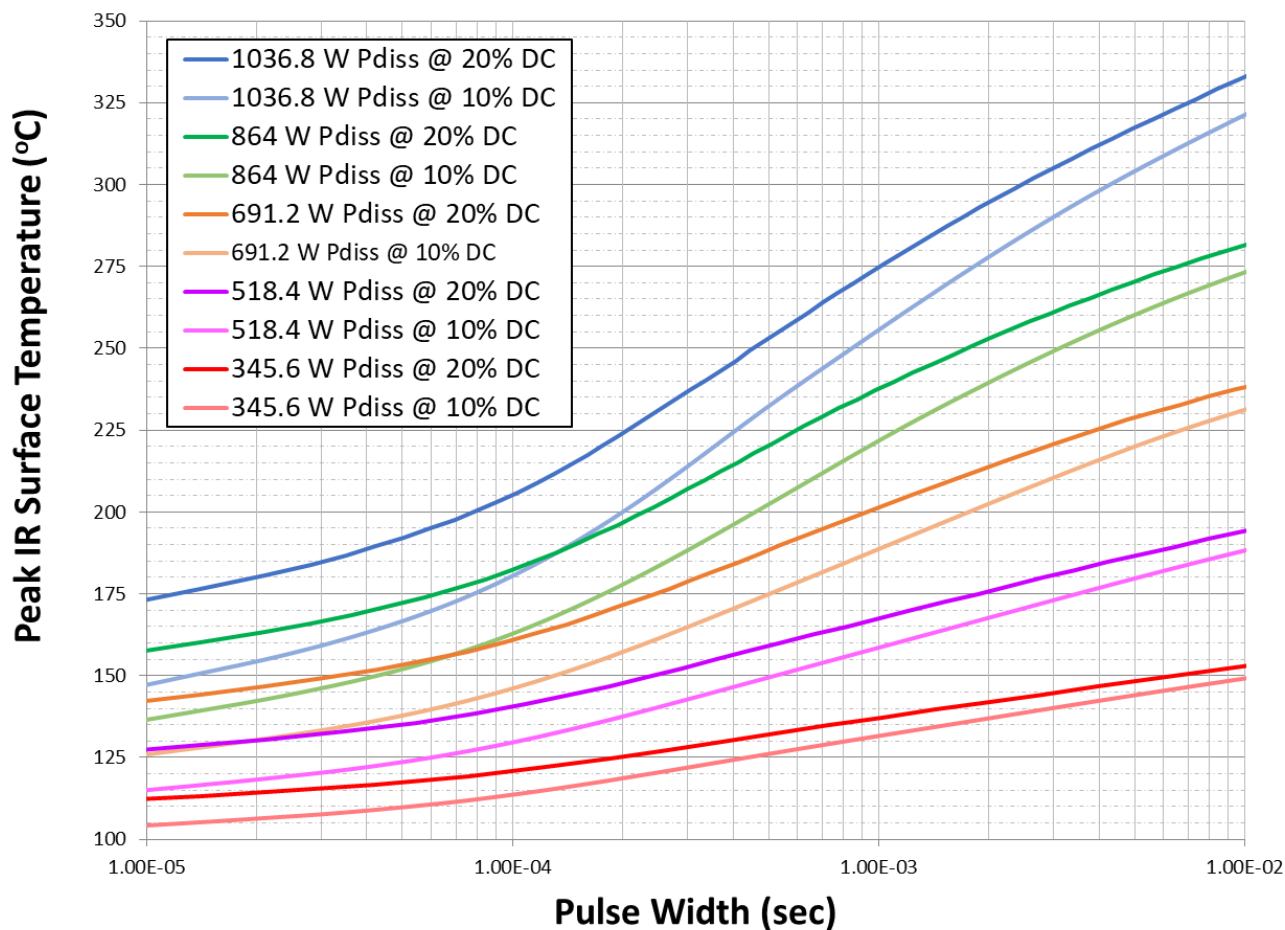
Symbol	Parameter	dB Compression	Typical
VSWR	Impedance Mismatch	3	5:1

Notes:

1. Test conditions unless otherwise noted: $V_D = 65$ V, $I_{DQ} = 1.5$ A (combined), Temp = +25 °C
2. Input drive power is determined at pulsed 3dB compression under matched condition at EVB output connector
3. Pulse: 100us, 10% Duty cycle

Thermal and Reliability Information – Pulsed ¹

Peak IR Surface Temperature vs. Pulse Width Pallet base fixed at 85 °C, Pdiss Varies



Parameter	Conditions	Values	Units
Thermal Resistance, IR ¹ (θ_{JC})	85 °C Pallet Backside Temperature	0.087	°C/W
Peak IR Surface Temperature ¹ (T_{ch})	Pdiss = 691.2 W, Pulse: 100 us PW, 10% DC	145	°C

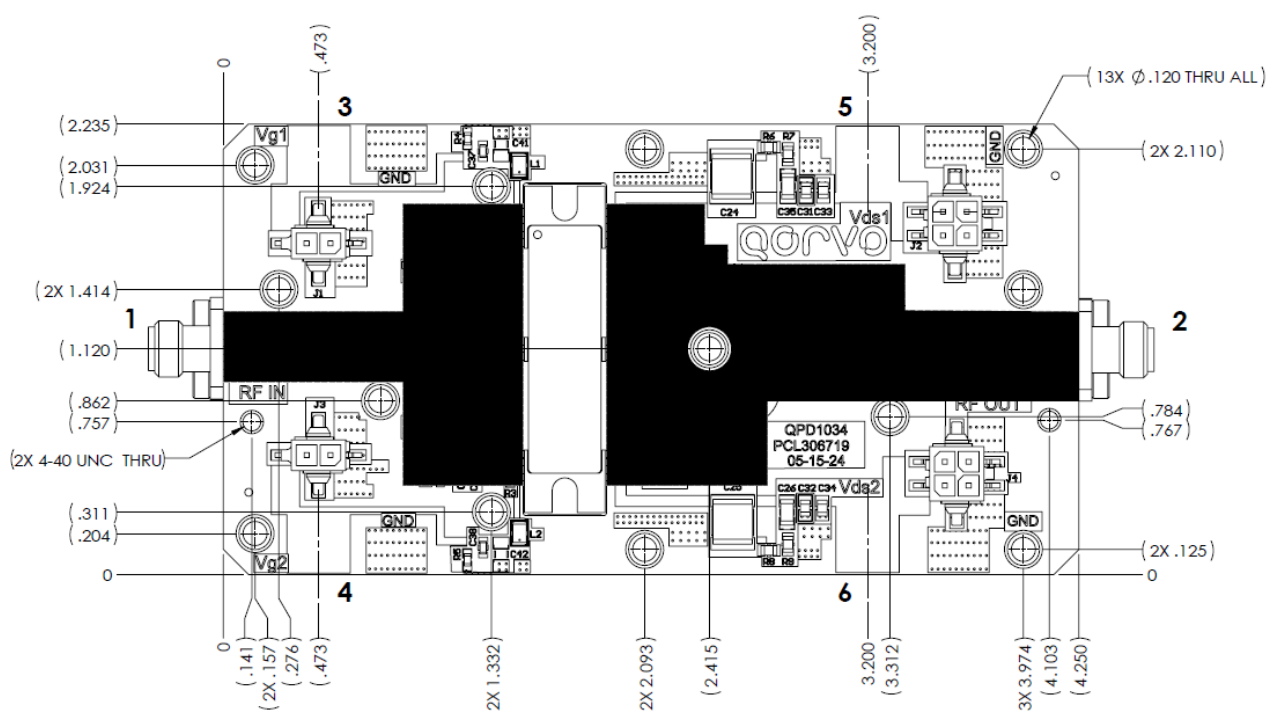
Notes:

1. Refer to the following document [GaN Device Channel Temperature, Thermal Resistance, and Reliability Estimates](#)

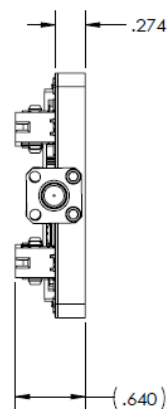
Package Dimensions and Pin Assignment ^{1,2}

Notes:

1. All dimensions are in inches.
2. Dimension tolerance is ± 0.005 inches, unless noted otherwise.

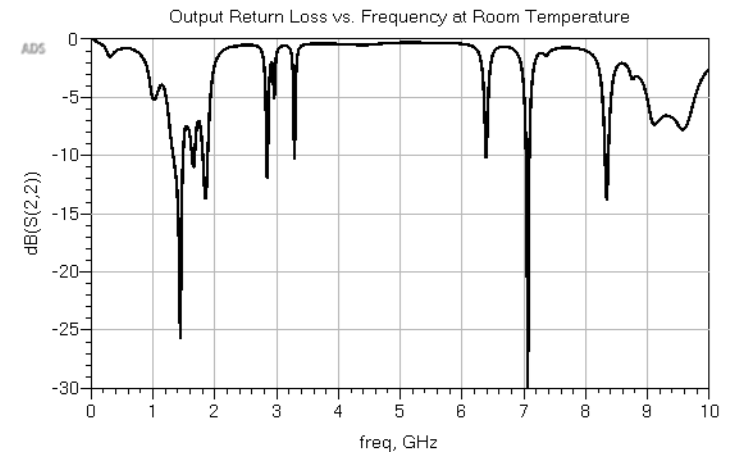
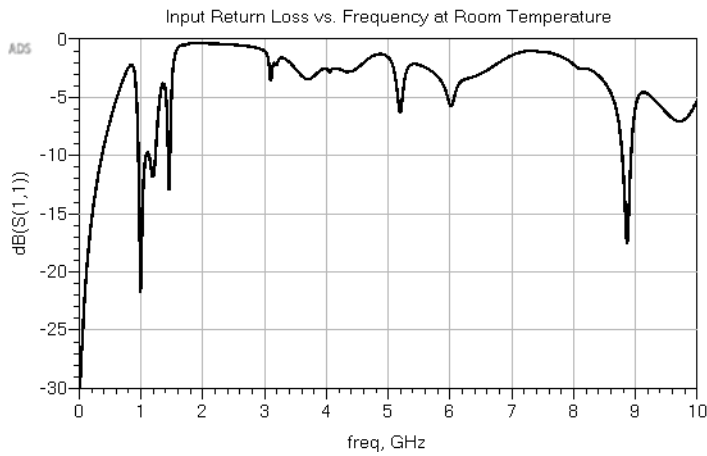
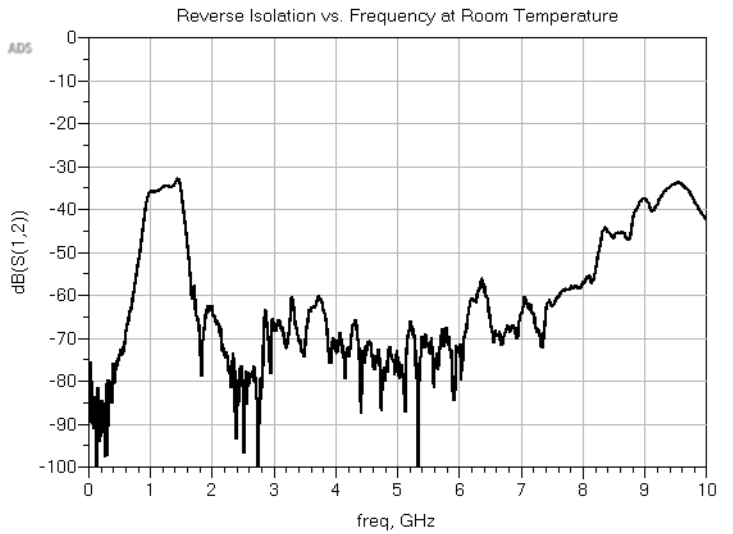
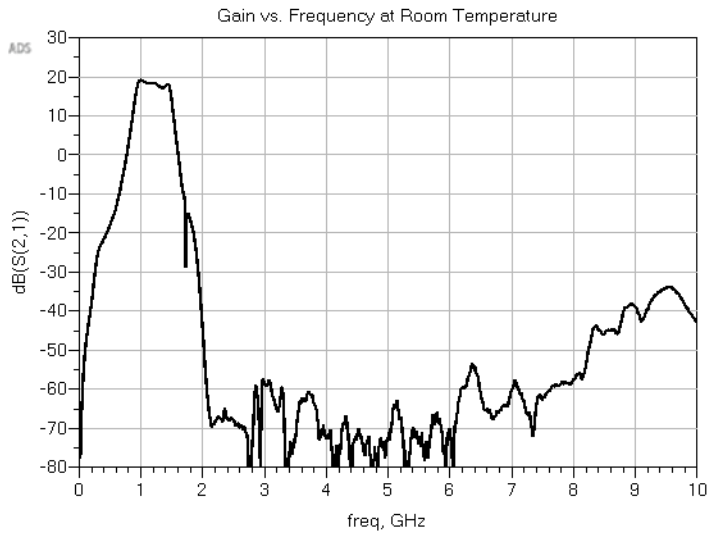


PIN ASSIGNMENT	
PIN	DEFINITION
1	RF IN
2	RF OUT
3	VG1
4	VG2
5	VD1
6	VD2



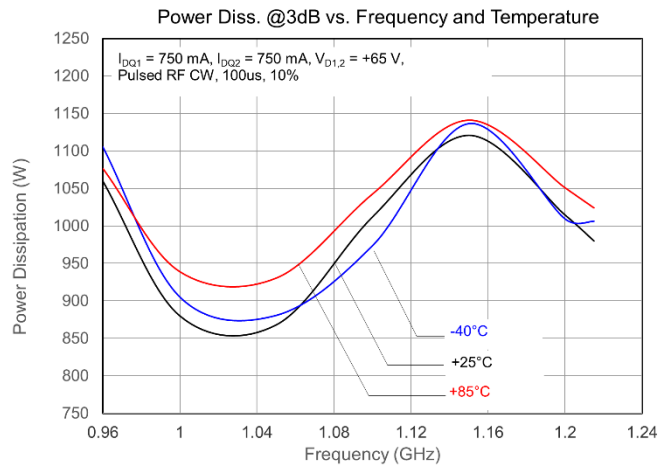
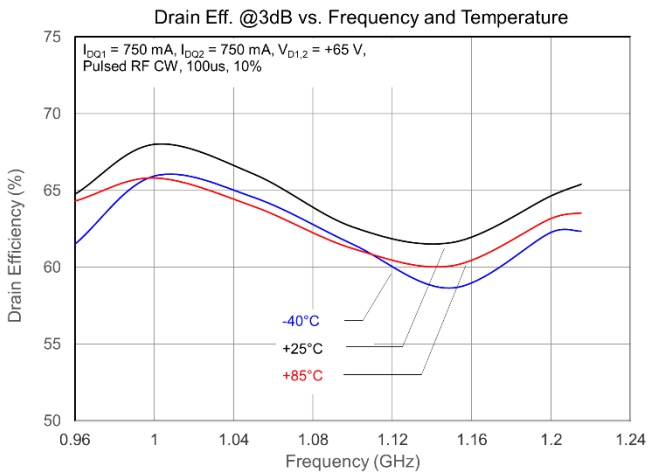
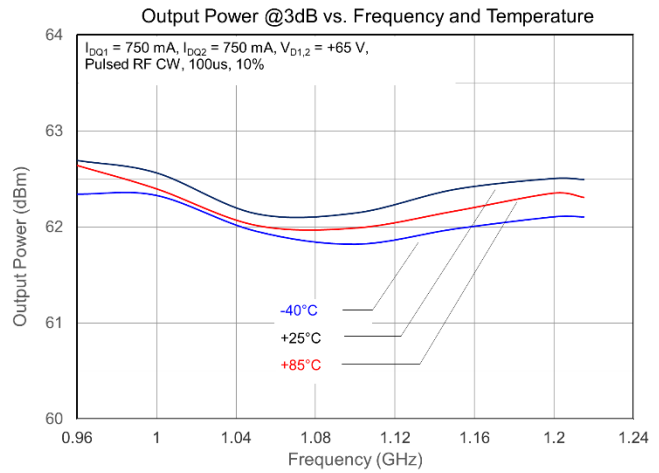
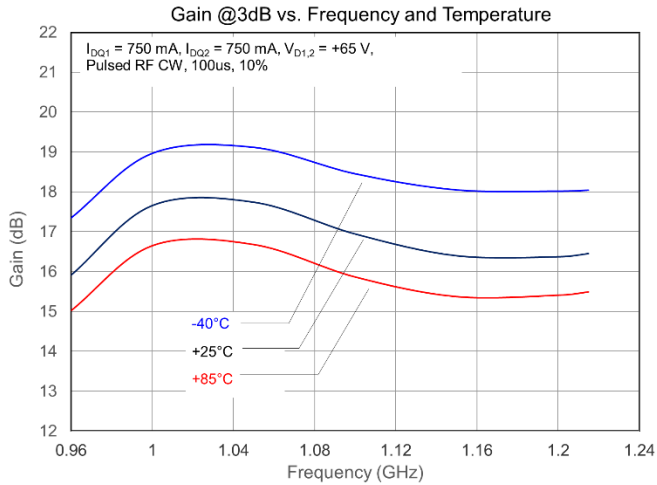
S-Parameters of 0.96 – 1.215 GHz Pallet at +25°C

Test Conditions unless otherwise noted: $V_D = 65$ V, $I_{DQ} = 1.5$ A, Temp = +25 °C.



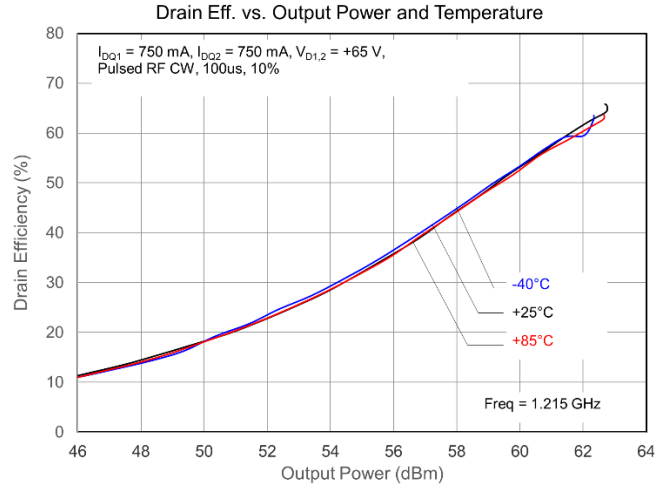
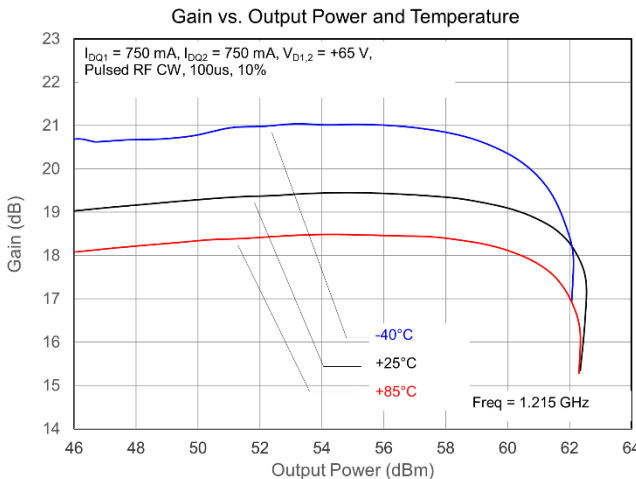
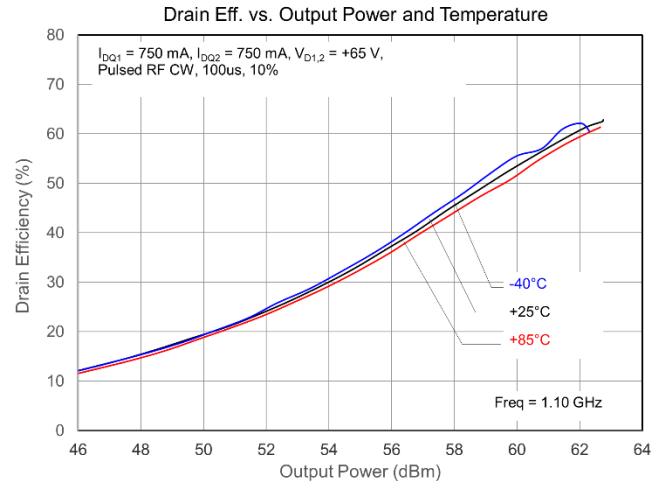
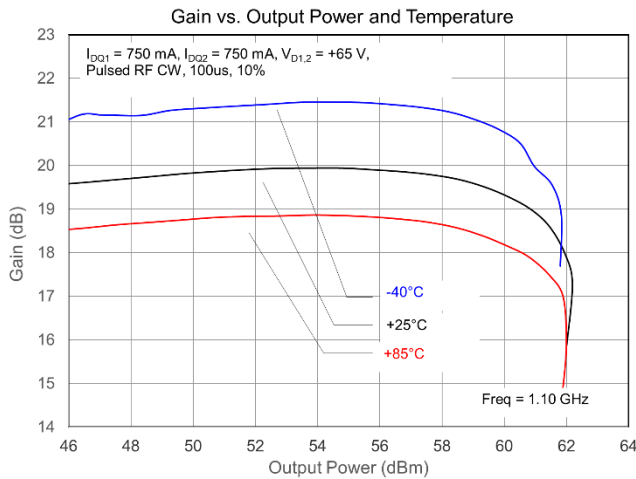
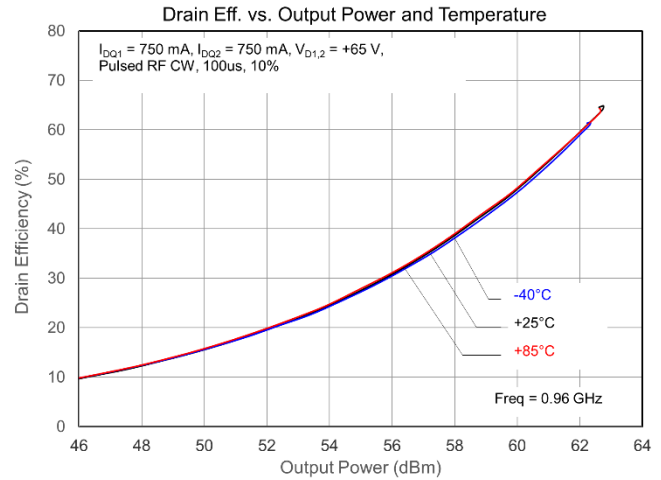
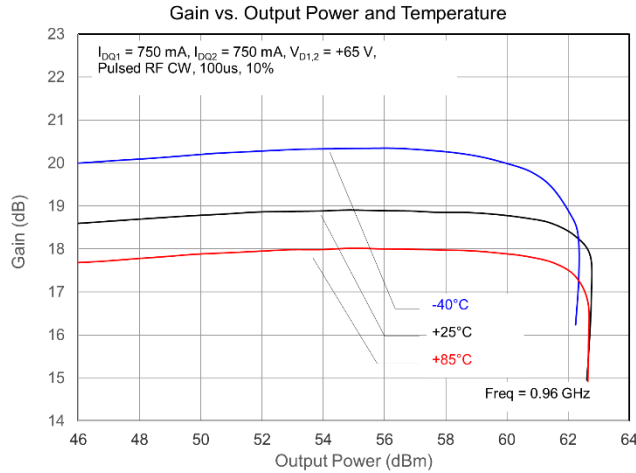
P3dB Performance over Temperature of 0.96 – 1.215 GHz Pallet

Test Conditions unless otherwise noted: $V_D = 65$ V, $I_{DQ} = 1.5$ A, 100 μ s Pulse Width, 10% Duty Cycle,



Power Drive-up Performance over Temperature of 0.96 – 1.215 GHz Pallet

Test Conditions unless otherwise noted: $V_D = 65$ V, $I_{DQ} = 1.5$ A, 100 μ s Pulse Width, 10% Duty Cycle



Handling Precautions

Parameter	Rating	Standard
ESD – Human Body Model (HBM)	Class 1C	ANSI/ESDA/JEDEC Standard JS-001
ESD – Charged Device Model (CDM)	Class C3	ANSI/ESDA/JEDEC Standard JS-002
MSL – Moisture Sensitivity Level	MSL3	IPC/JEDEC Standard J-STD-020



Contact Information

For the latest specifications, additional product information, worldwide sales and distribution locations:

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