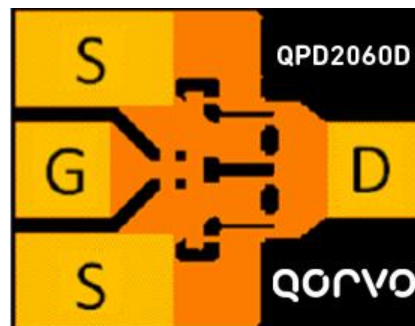


### Product Overview

The Qorvo QPD2060D is a discrete 600 micron pHEMT which operates from DC to 20 GHz. The QPD2060D is fabricated using Qorvo's proven standard 0.25 um power pHEMT production process. This process features advanced techniques to optimize microwave power and efficiency at high drain bias operating conditions.

The QPD2060D typically provides 28 dBm of output power at P1dB with gain of 12 dB and 55% power-added efficiency at 1 dB compression. This performance makes the QPD2060D appropriate for high efficiency applications. The protective overcoat layer with silicon nitride provides a level of environmental robustness and scratch protection.

The QPD2060D is lead-free and RoHS compliant.



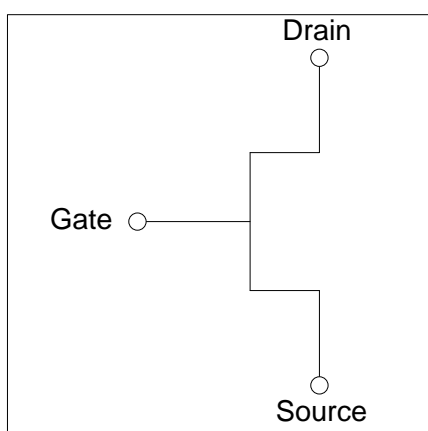
### Key Features

- Frequency: DC – 20 GHz
- Output Power ( $P_{1dB}$ )<sup>1</sup>: 28 dBm
- Typical Gain<sup>1</sup>: 12 dB
- Typical PAE<sub>1dB</sub><sup>1</sup>: 55%
- Noise Figure<sup>1</sup>: 1.4 dB
- No Vias
- Technology: 0.25 um GaAs pHEMT
- Chip Dimensions: 0.41 x 0.34 x 0.10 mm

Notes:

1. @ 12 GHz

### Functional Block Diagram



Pad Dimensions	Terminal
71 um x 71 um	Gate
71 um x 71 um	Drain
113.8 um x 71 um	Source

### Applications

- Defense and Aerospace
- High-Reliability
- Test and Measurement
- Commercial
- Broadband Wireless

### Ordering Information

Part Number	Description
QPD2060D	600 um GaAs pHEMT

### Absolute Maximum Ratings<sup>1</sup>

Parameter	Absolute	Continuous	Units
Drain-Source Voltage ( $V_{DS}$ ) <sup>(2)</sup>	12	8	V
Gate-Source Voltage ( $V_{GS}$ )	-7	-3	V
Drain Current ( $I_{DS}$ ) <sup>(2)</sup>	$I_{DSS}$	$I_{DSS}$	mA
Forward Gate Current ( $I_{G,F}$ )	30	5	W
Channel Temperature ( $T_{CH}$ ) <sup>(3)</sup>	175 <sup>(4)</sup>	150 <sup>(5)</sup>	°C
Storage Temperature ( $T_{STG}$ )	-65 to 150	-65 to 150	°C
RF Input Power ( $P_{IN}$ ) <sup>(2)</sup>	24	At 3dB Compression	dBm
Power Dissipation ( $P_{TOT}$ )	3.2	2.1	W

Notes:

- These ratings represent the maximum operable values for this device. Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device and/or affect device lifetime. These are stress ratings only, and functional operation of the device at these conditions is not implied.
- Combinations of supply voltage, supply current, input power, and output power shall not exceed the maximum total power dissipation listed in the table.
- Junction operating temperature will directly affect the device median time to failure. For maximum life, it is recommended that junction temperatures be maintained at the lowest possible levels.
- When operated at this channel temperature, the median life is 1.0E+5 hours.
- When operated at this channel temperature, the median life is 1.0E+6 hours.

### Electrical Characteristics<sup>1</sup>

Parameters	Conditions	Typical Values	Units
Output Power at 1dB Compression ( $P_{1dB}$ )	Freq = 12 GHz	28	dBm
Gain at P1dB ( $G_{1dB}$ )	$V_{DS} = 8\text{ V}$	12	dB
PAE at P1dB (PAE)	$I_{DS} = 50\% I_{DSS}$	55	%
50Ω Noise Figure (NF)	Freq = 12 GHz, $V_{DS} = 3\text{ V}$ , $I_{DS} = 25\text{ mA}$	1.4	dB
Saturated Drain Current ( $I_{DSS}$ )	$V_{DS} = 2\text{ V}$ , $V_{GS} = 0\text{ V}$	194 <sup>(2)</sup>	mA
Transconductance ( $G_m$ )	$V_{DS} = 2\text{ V}$ , $I_{DS} = 50\% I_{DSS}$	232	mS
Pinch-Off Voltage ( $V_P$ )	$V_{DS} = 2\text{ V}$ , $I_{DS} = 0.30\text{ mA}$	-1.0	V
Gate-Drain Breakdown Voltage ( $BV_{GD}$ )	$I_G = 0.60\text{ mA}$ , source open	-15	V
Gate-Source Breakdown Voltage ( $BV_{GS}$ )	$I_G = 0.60\text{ mA}$ , drain open	-15	V
Thermal Resistance ( $R_{TH}$ ) <sup>(3)</sup>	AuSn eutectic attach	54	°C/W

Notes:

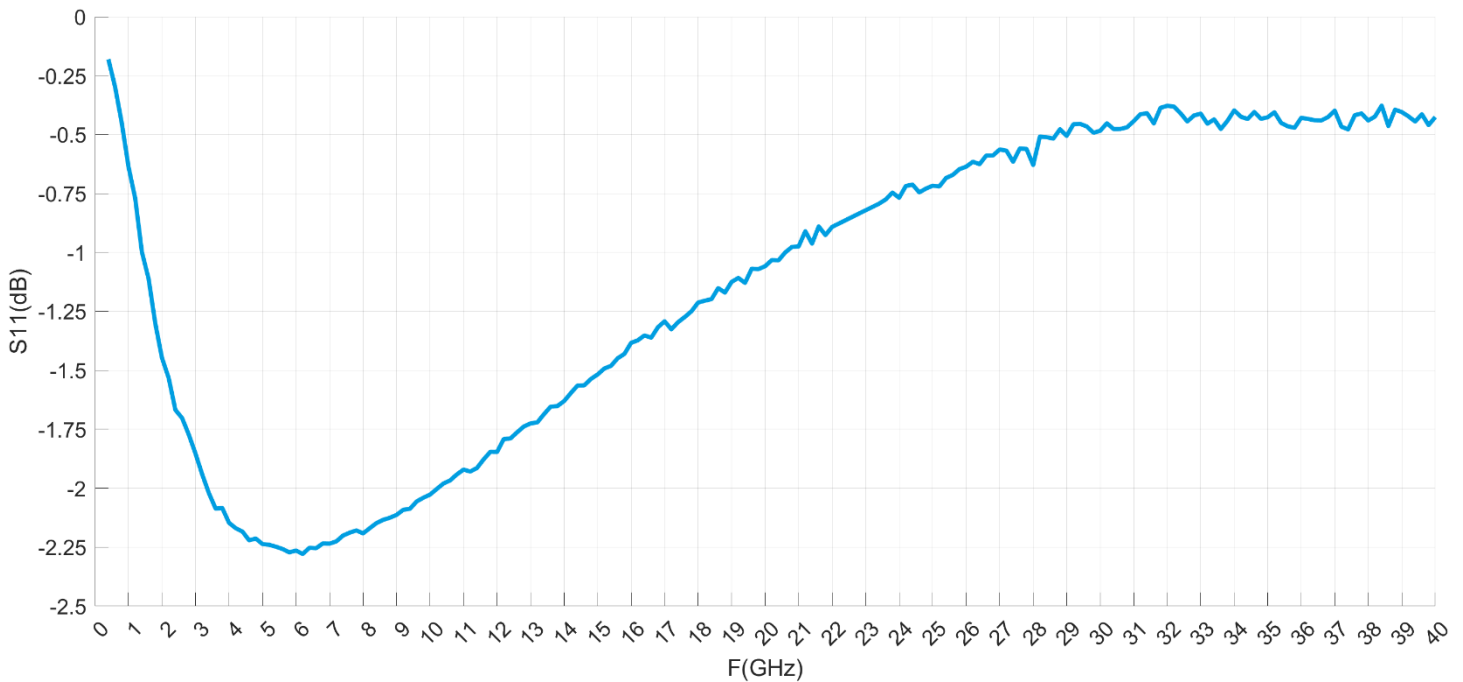
- Test conditions unless otherwise noted:  $T_A = +25\text{ °C}$
- Typical Standard Deviation of 2 mA (1  $\sigma$ ).
- Based on IR Scan.

## S-Parameters<sup>1</sup>

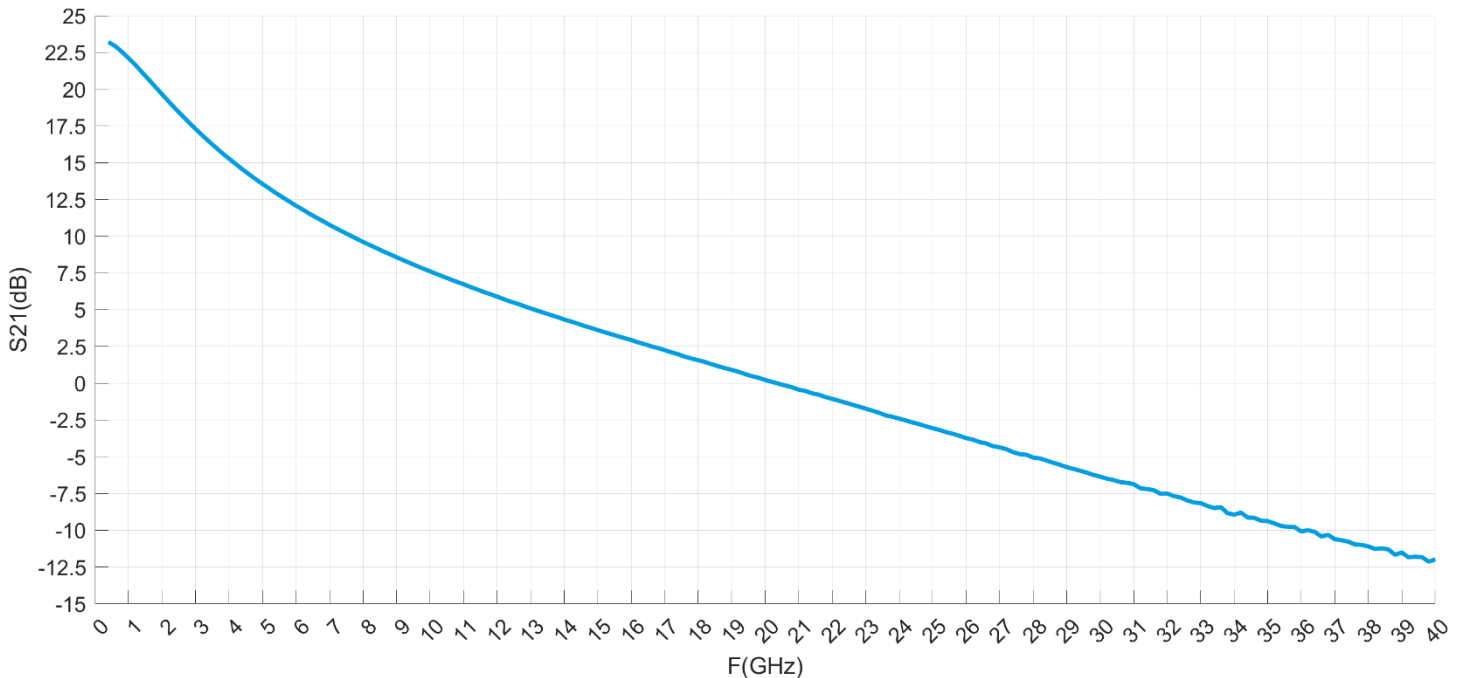
### Notes:

1. S-Parameter data was measured by Modelithics with bonding wires at  $V_D = 8$  V,  $I_{DQ} = 48$  mA, and  $T_A = 25$  °C. Please review Modelithics's QPD2060D model datasheet for more information on configurations of bonding wires.

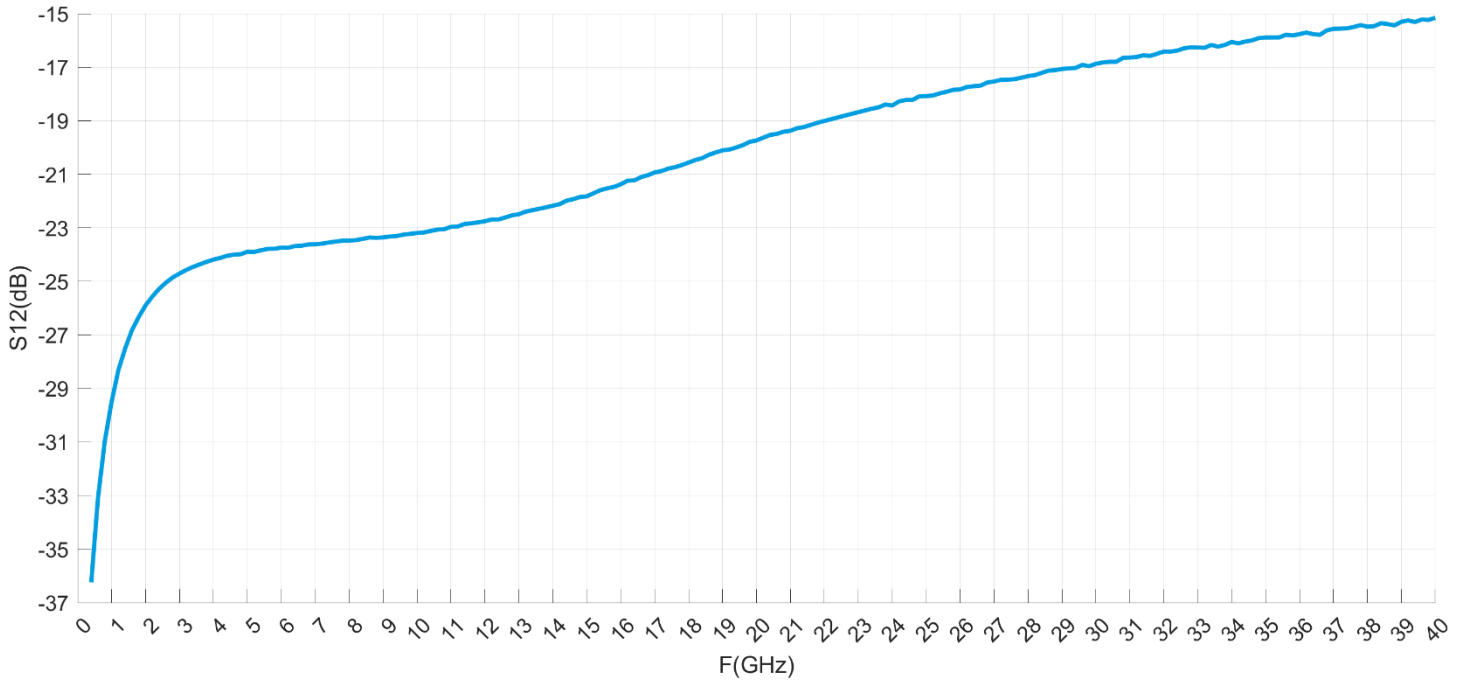
**S11**



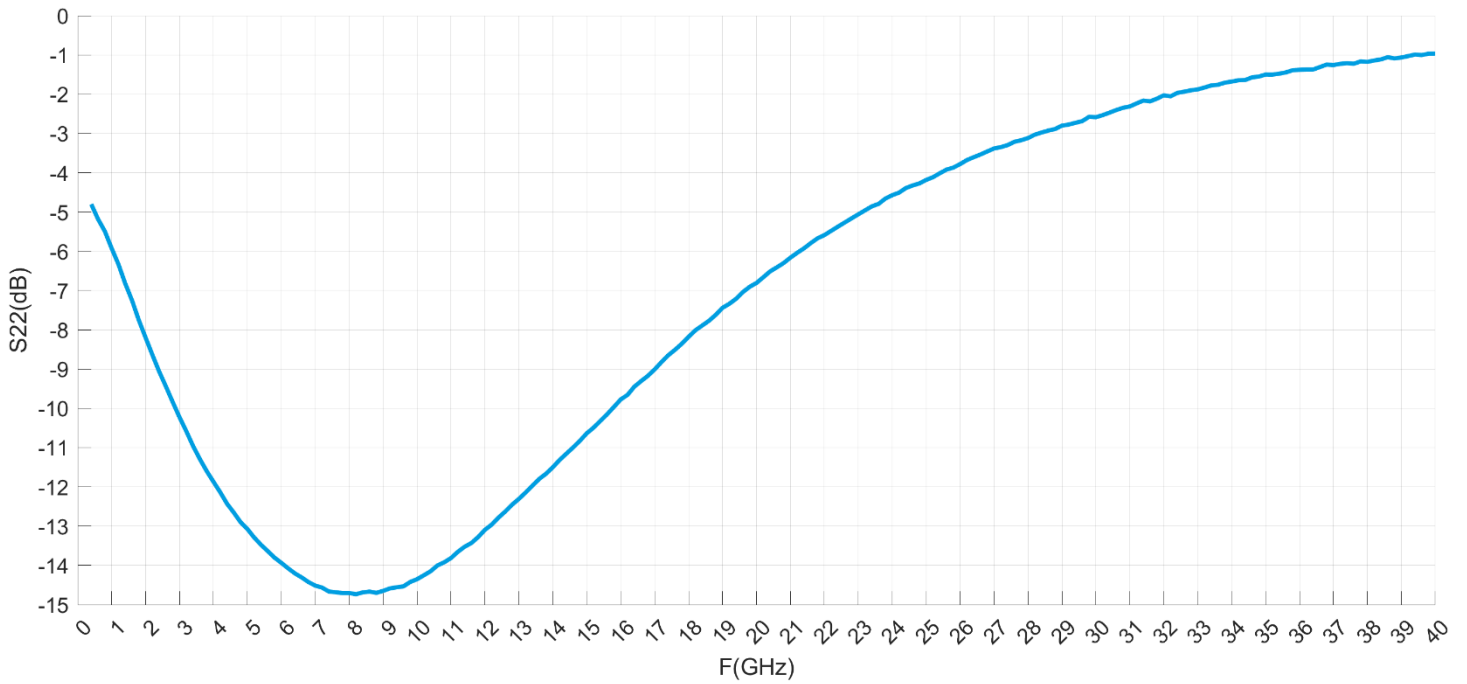
**S21**



**S12**



**S22**

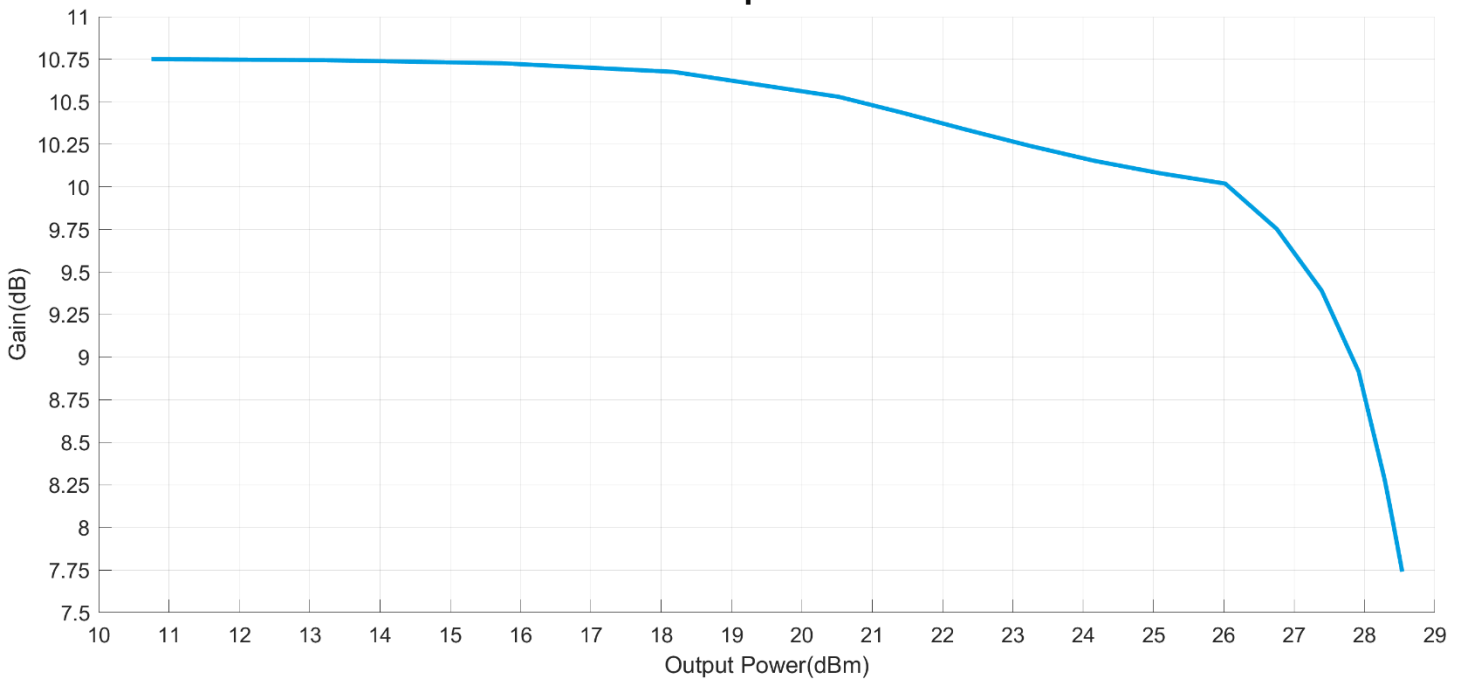


### Performance Plots – Power Tuned @ 12 GHz<sup>1</sup>

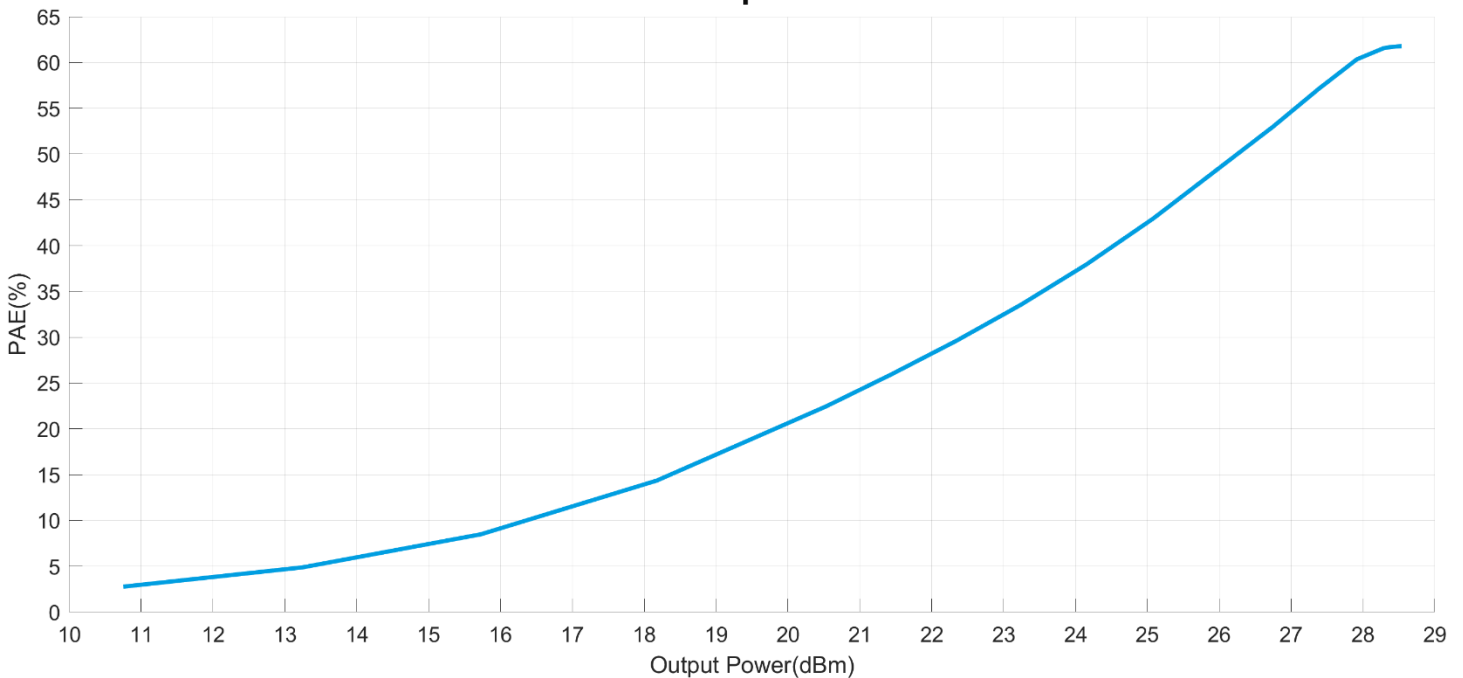
#### Notes:

1. Performance Plots are simulated result using Modelithics's QPD2060D model with  $V_D = 8$  V,  $I_{DQ} = 48$  mA,  $BW_{removal} = 0$ . Please visit Modelithics for Measurement vs. Model Data and more information.

#### Gain vs. Output Power



#### PAE vs. Output Power

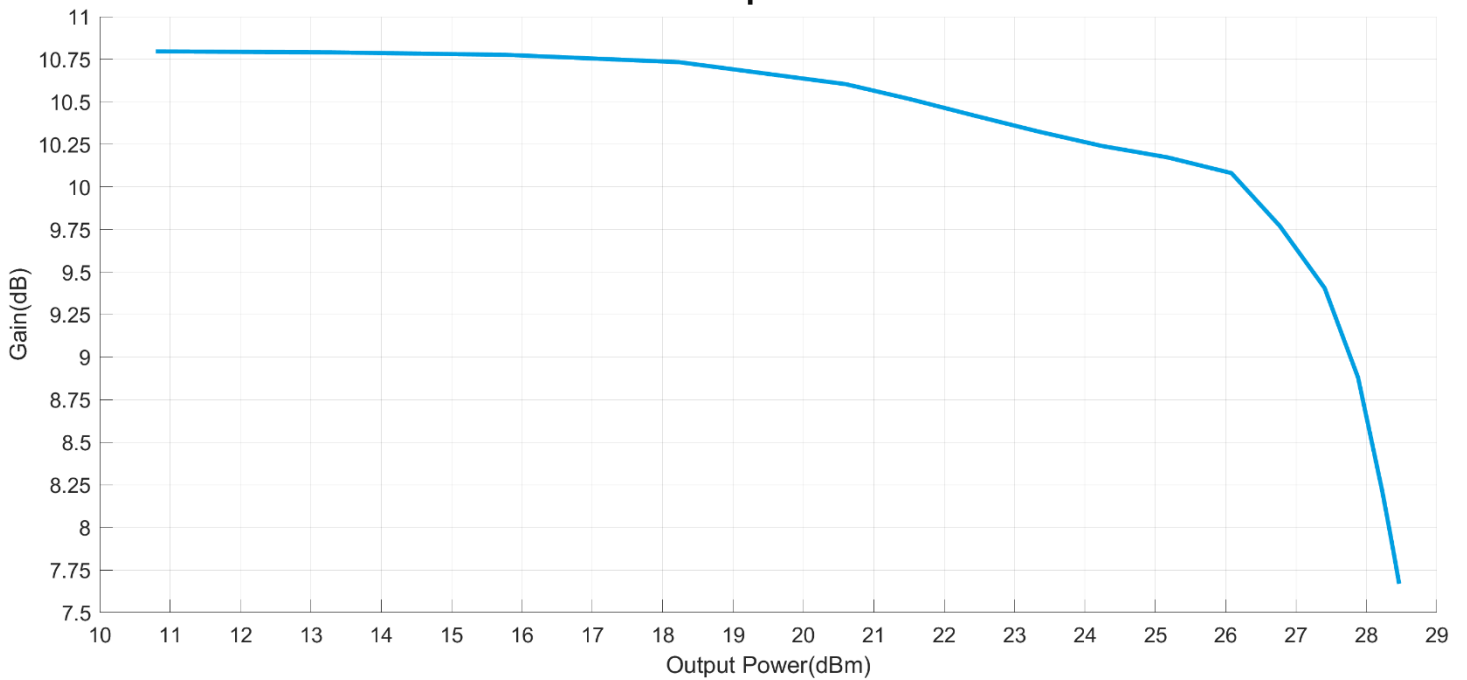


### Performance Plots – Efficiency Tuned @ 12 GHz<sup>1</sup>

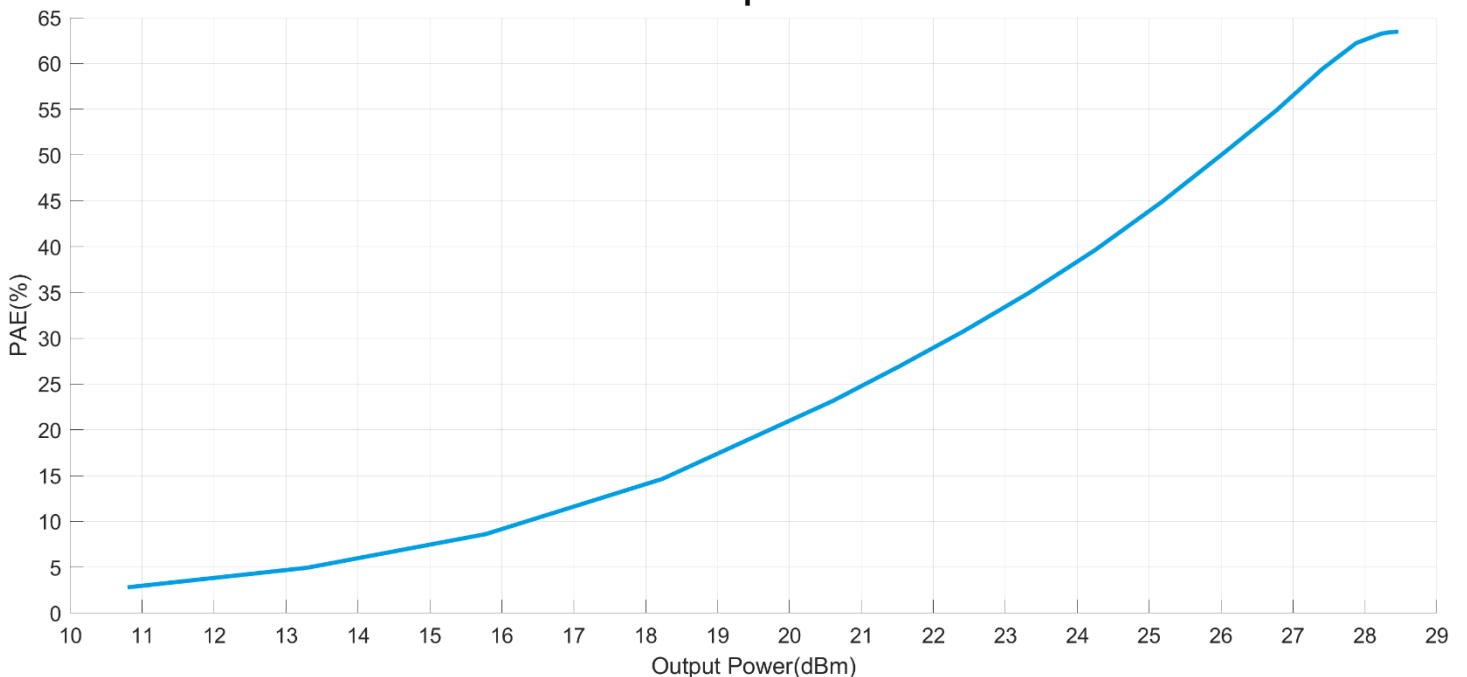
#### Notes:

1. Performance Plots are simulated result using Modelithics's QPD2060D model with  $V_D = 8$  V,  $I_{DQ} = 48$  mA,  $BW_{removal} = 0$ . Please visit Modelithics for Measurement vs. Model Data and more information.

#### Gain vs. Output Power



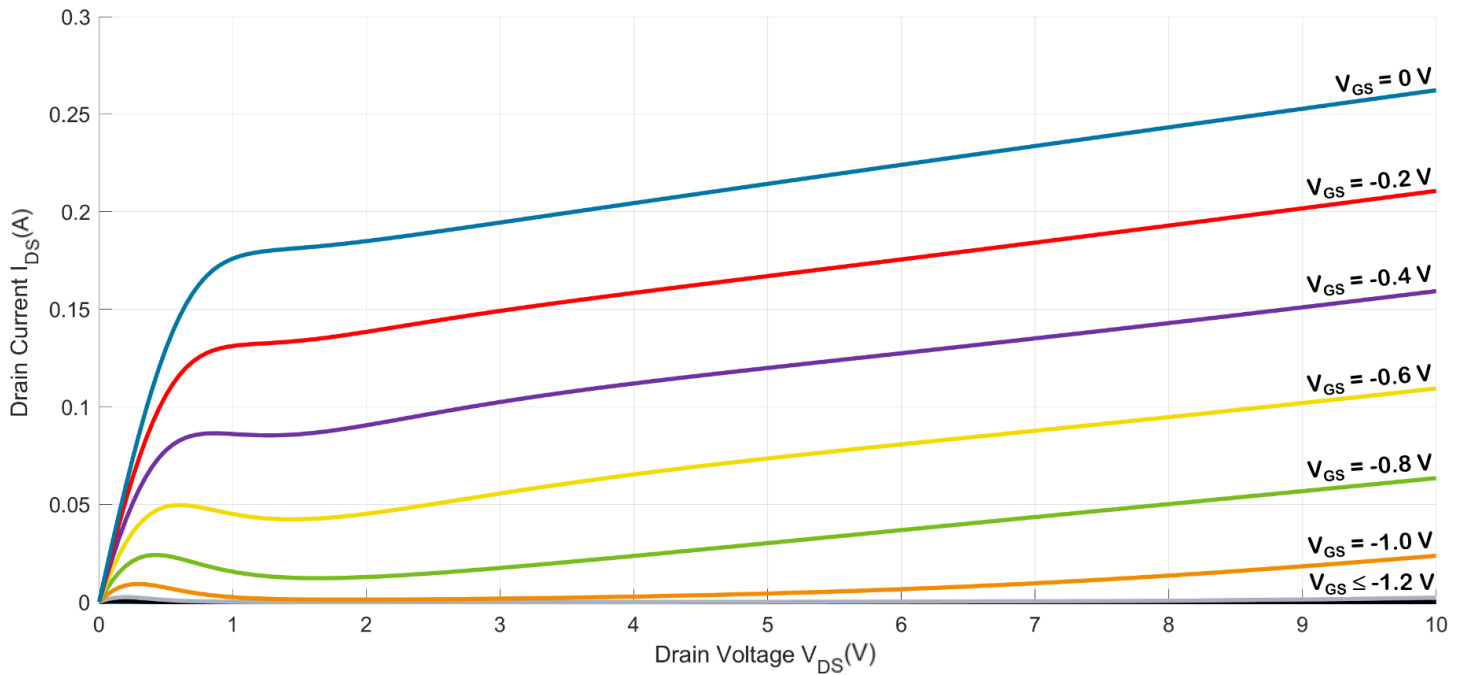
#### PAE vs. Output Power



PIN	X	Y
1	0.071	0.071
2	0.1138	0.071
3	0.071	0.071
4	0.1138	0.071

[www.gorvo.com](http://www.gorvo.com)

## DC Characteristics<sup>1</sup>



### Notes:

1. The I-V plot shown above was simulated data using Modelithics's model with Model self\_heat = 0. Please visit Modelithics for Measurement vs. Model Data and more information.



### Assembly Notes

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Component placement and adhesive attachment assembly notes:

- Vacuum pencils and/or vacuum collets are the preferred method of pick up.
- Air bridges must be avoided during placement.
- The force impact is critical during auto placement.
- Organic attachment (i.e. epoxy) can be used in low-power applications.
- Curing should be done in a convection oven; proper exhaust is a safety concern.

Reflow process assembly notes:

- Recommend Eutectic die attach with AuSn (80/20) solder and limit exposure to temperatures above 300°C to 30 seconds, maximum.
- An alloy station or conveyor furnace with reducing atmosphere should be used.
- Do not use any kind of flux.
- Coefficient of thermal expansion matching is critical for long-term reliability.
- Devices must be stored in a dry nitrogen atmosphere.

Interconnect process assembly notes:

- Either Thermo-compression Wedge Bonding or Thermosonic Ball Bonding can be used to bond onto the die.
- Force, time, and ultrasonics are critical bonding parameters.
- Aluminum wire should not be used.
- Devices with small pad sizes should be bonded with 0.0008-inch wire.

### Handling Precautions

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GaAs devices are susceptible to damage for electrostatic discharge. Proper precautions should be observed during handling, assembly, and test.



Caution!  
ESD-Sensitive Device

### RoHS Compliance

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This part is compliant with 2011/65/EU RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment) as amended by Directive 2015/863/EU.

This product also has the following attributes:

- Lead Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A (C<sub>15</sub>H<sub>12</sub>Br<sub>4</sub>O<sub>2</sub>) Free
- PFOS Free
- SVHC Free

Not HAST compliant.

### Contact Information

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For the latest specifications, additional product information, worldwide sales and distribution locations:

Web: [www.qorvo.com](http://www.qorvo.com)

Tel: 1-844-890-8163

Email: [customer.support@qorvo.com](mailto:customer.support@qorvo.com)

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