QP8817AEVB01 User's Guide

Description

This document describes the characteristic and operation of the Qorvo QP8817AEVB01 evaluation kit (EVK). It provides setup and operation instructions, schematic, layout, BOM, and test data. This EVK demonstrates the QP8817A Qorvo PMU power management IC. Other QP8817x options can be evaluated on this EVK by replacing the IC and any other necessary components.

Features

The EVK can be used as a standalone board if desired. However, to access the internal registers and to take full advantage of the IC's capability, the user must connect the EVK kit to a PC with Qorvo's USB-TO-I2C interface dongle and use the GUI software. The EVK provides full access to each converter's input and output voltage, as well as all the digital control signals. This gives the user the flexibility to configure the EVK to match their real-world system.

Note that the QP8817AEVB01 is specifically configured for the QP8817A.



Figure 1 – EVK Picture

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Setup

Required Equipment

QP8817 EVK

USB-TO-I2C Dongle

Power supply – 3.8V @ 8A for full power operation

Oscilloscope – >100MHz, >4 channels

Loads – Electronic or resistive. 12A minimum current capability.

Digital Multi-meters (DMM)

Windows compatible computer with spare USB port.

EVK Setup

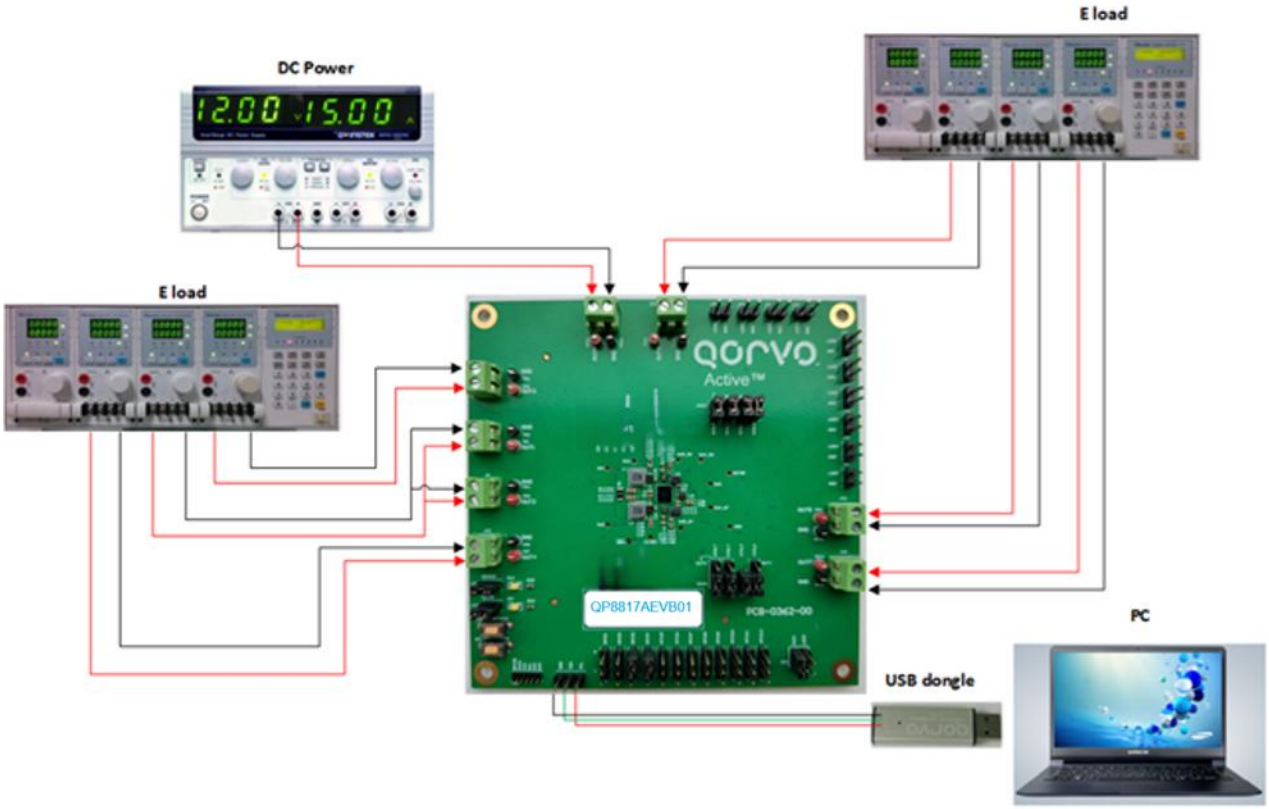


Figure 2 – EVK Setup

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Hardware Setup

1. Decide which voltage will power VIO_IN. Qorvo recommends powering VIO from the AVIN input. Connect a shorting jumper between J49-1 and J49-2 header to power VIO from the AVIN input voltage.
2. Connect a jumper between J6-1 and J6-2 header to power VIN_LDO1 from AVIN input voltage.
3. Connect a jumper between J8-1 and J8-2 header to power VIN_LDO2 from AVIN input voltage.
4. Connect a jumper between J10-1 and J10-2 header to power VIN_LDO3 from AVIN input voltage.
5. Connect a jumper between J13-1 and J13-2 header to power VIN_LDO4 from AVIN input voltage.
6. Connect a jumper between J7-1 and J7-2 header to power VIN_LDO5 from AVIN input voltage.
7. Connect a jumper between J9-1 and J9-2 header to power VIN_LDO6 from AVIN input voltage.
8. Connect a jumper between J11-1 and J11-2 header to power VIN_LSW7 from AVIN input voltage.
9. Connect a jumper between J14-1 and J14-2 header to power VIN_LSW8 from AVIN input voltage.
10. Connect a jumper between J43-1 and J43-2 header for GPIO9 for D11.
11. Connect a jumper between J44-1 and J44-2 header for GPIO10 for D12.
12. Connect a jumper between J25-1 and J25-2 header for GPIO6.
13. Connect a jumper between J36-1 and J36-2 header for GPIO4.
14. Connect a jumper between J37-1 and J37-2 header for GPIO3.
15. No jumpers are needed for GPIO1/2/5/7/8.
16. Connect an appropriate load to each power supply output.
17. Note that the typical setup is to apply the same 3.8V input voltage to all inputs. Using different input voltage sources requires careful consideration of startup sequencing.



Figure 3 – Shorting Jumper Settings

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GUI Setup (optional)

1. Refer to the end of this document for detailed instructions to install the QP8817 GUI.
2. Connect the USB-TO-I2C dongle to the computer via a USB cable.
3. Connect the USB-TO-I2C dongle to the EVK J42 connector. Refer to Figure 4 to ensure the correct polarity of the connection. As a guide, use the “Active-Semi” logo on the top of the dongle so the black wire is connected toward the lower left corner of the Dongle.

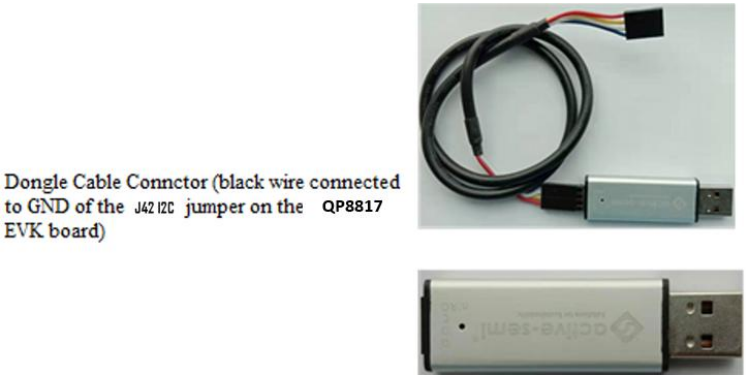


Figure 4 – USB-TO-I2C Dongle Connection

EVK Design Parameters

The QP8817AEVB01 is designed for a 3.8V input voltage. Table1 shows the Regulators’ output voltage and the current supply capability.

Table 1. EVK Design Parameters

Parameter	Description	Min	Typ	Max	Unit
VIN	Operation Input range of Power Supply	2.7	3.8	5	V
OUT1/2	Buck1/2 output voltage (Turned on by BT1)		0.725		V
OUT3	Buck3 output voltage		1.8		V
OUT4	Buck4 output voltage		1.2		V
OUT5	Buck5 output voltage		0.725		V
BB6	BB6 output voltage (Controlled by GPIO4)		3.3		V
BB7	BB7 output voltage		3.3		V
LDO1	LDO1 output voltage		1.9		V
LDO2	LDO2 output voltage (Turned on by ON Bit)		1.8		V
LDO3	LDO3 output voltage (LSW)		/		V
LDO4	LDO4 output voltage		1.8		V
LDO5	LDO5 output voltage		1.05		V
LDO6	LDO6 output voltage (LSW) (Turned on by ON Bit)		/		V
LSW7	LSW7 output voltage (Controlled by GPIO3)		/		V
LSW8	LSW8 output voltage (Turned on by ON Bit)		/		V
LDO9	LDO9 output voltage		1.8		V
LDO10	LDO10 output voltage		3.3		V

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Io_OUT1	Buck1 load current		5		A
Io_OUT2	Buck2 load current		5		A
Io_OUT3	Buck3 load current		1		A
Io_OUT4	Buck4 load current		1		A
Io_OUT5	Buck5 load current		1		A
Io_OUT6	BB6 load current		1		A
Io_OUT7	BB7 load current		1		A
Io_LDO1	LDO1 load current		0.1		A
Io_LDO2	LDO2 load current		0.1		A
Io_LDO3	LDO3 load current		/		A
Io_LDO4	LDO4 load current		0.05		A
Io_LDO5	LDO5 load current		0.05		A
Io_LDO6	LDO6 load current		/		A
Io_LSW7	LSW7 load current		/		A
Io_LSW8	LSW8 load current		/		A
Io_LDO9	LDO9 load current		0.01		A
Io_LDO10	LDO10 load current		0.005		A

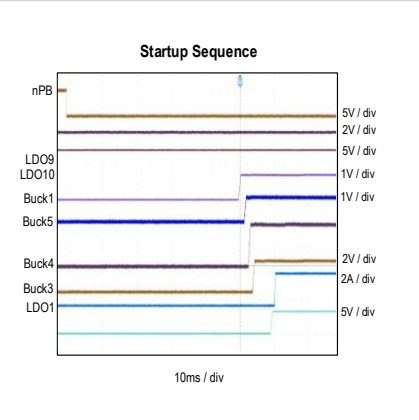
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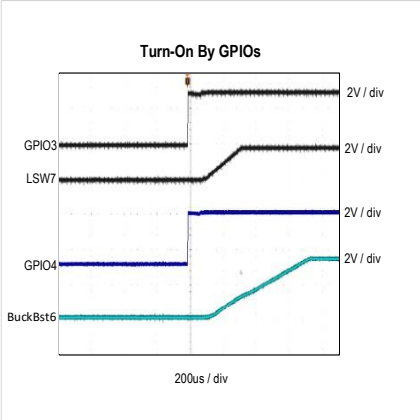
EVK Operation

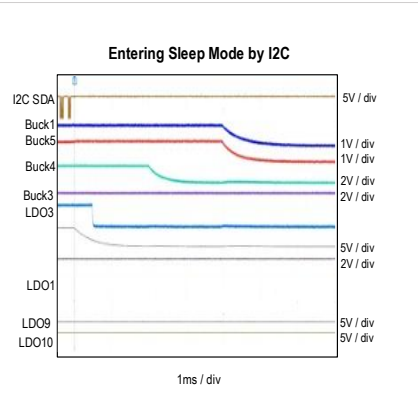
Turn-on

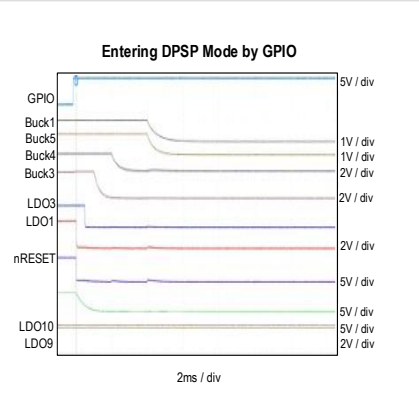
Apply the 3.8V input voltage. Press the push button BT1 and All outputs rails automatically turn on with the programmed startup sequence. LSW7 and BuckBoost6 can be turned on by a logic high signal on GPIO3 and GPIO4 respectively, Other rails that are off by default can be turned on by setting the ON bit to 1 in their respective register using the GUI.

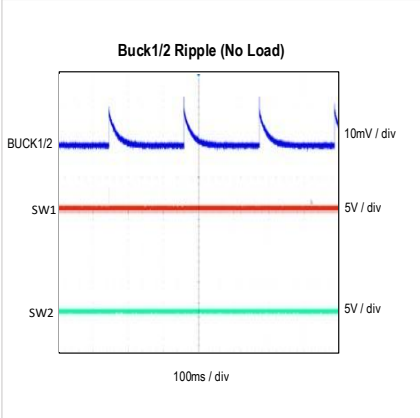
Test Results

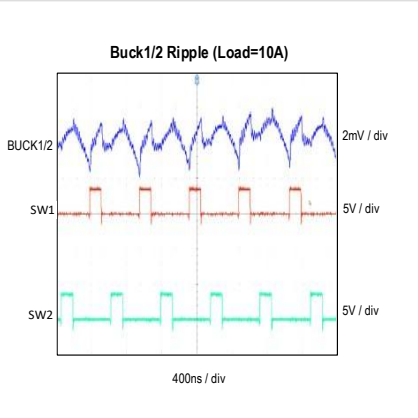






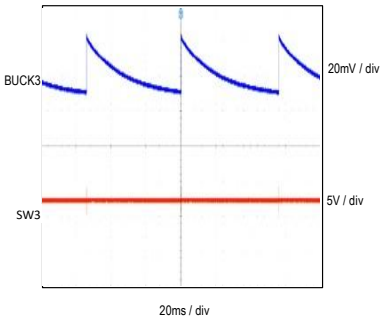




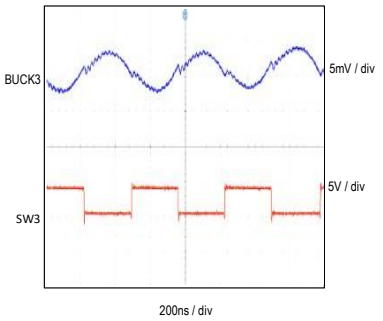


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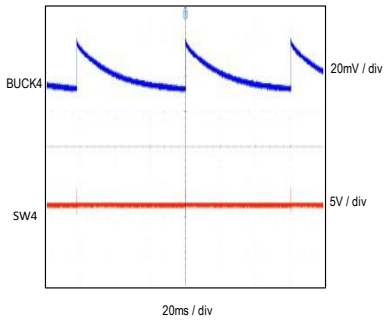
Buck3 Ripple (No Load)



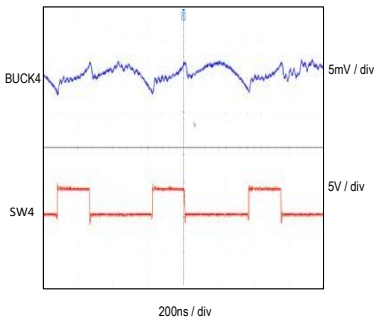
Buck3 Ripple (Load=1A)



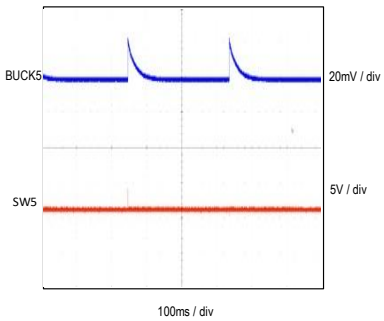
Buck4 Ripple (No Load)



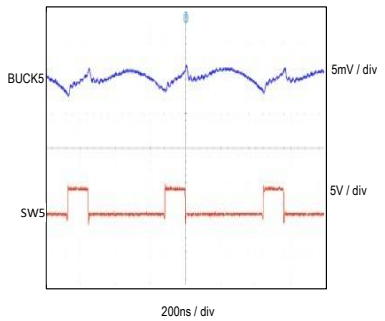
Buck4 Ripple (Load=1A)



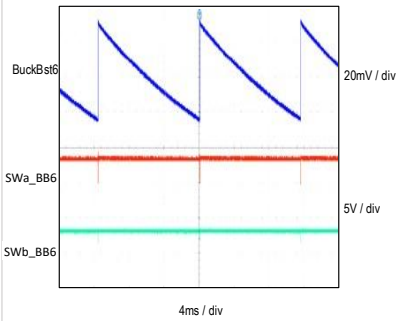
Buck5 Ripple (No Load)



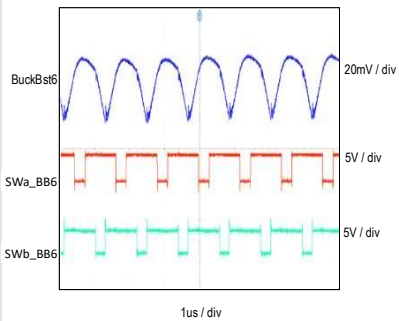
Buck5 Ripple (Load=1A)



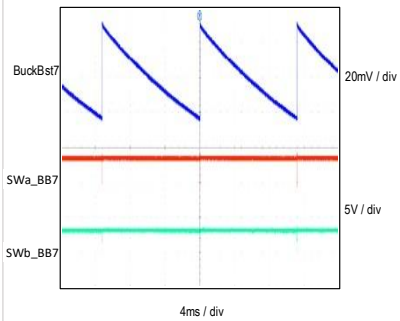
BuckBoost6 Ripple (No Load)



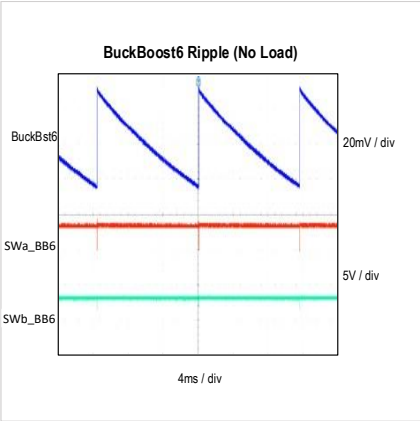
BuckBoost6 Ripple (Load=1A)

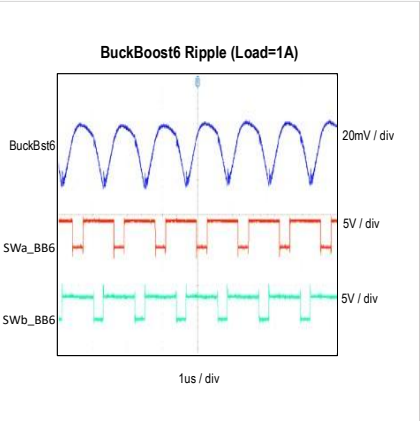


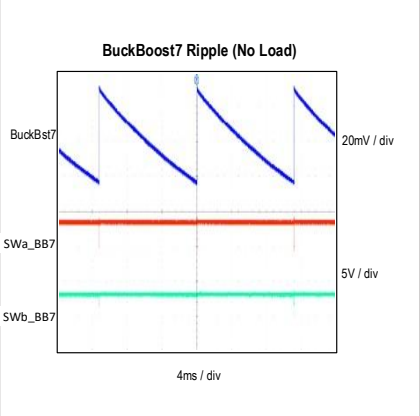
BuckBoost7 Ripple (No Load)

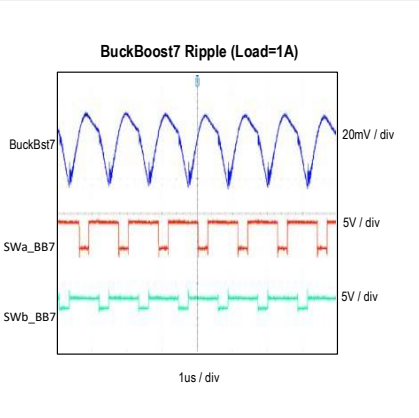


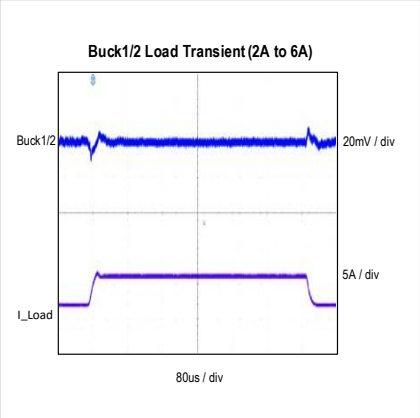
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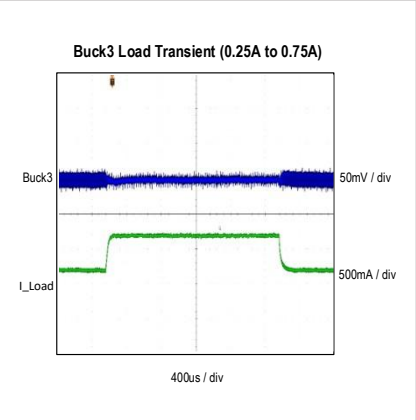


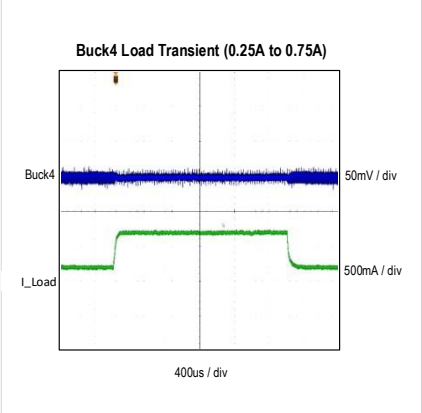


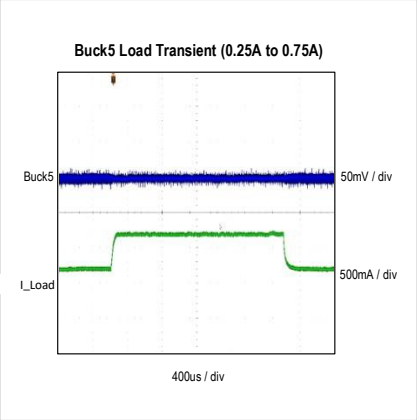


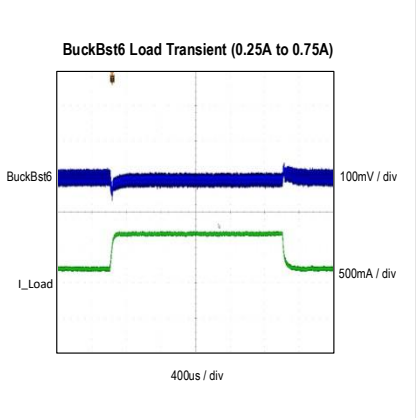


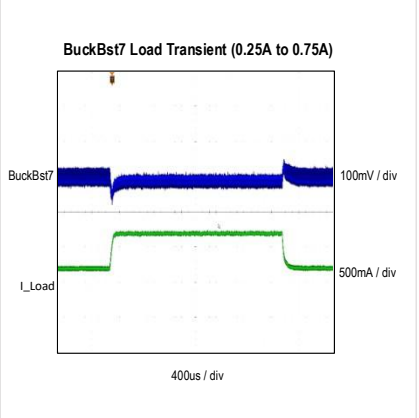
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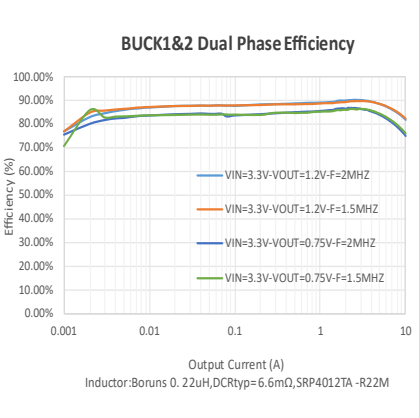


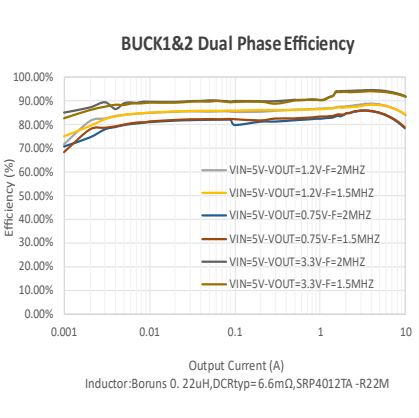




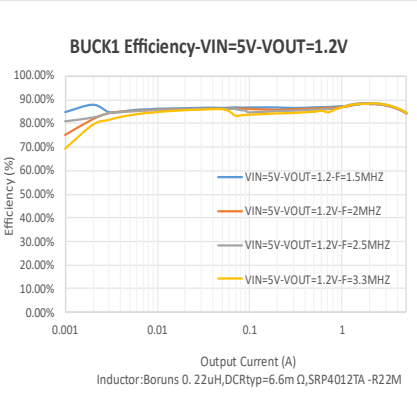


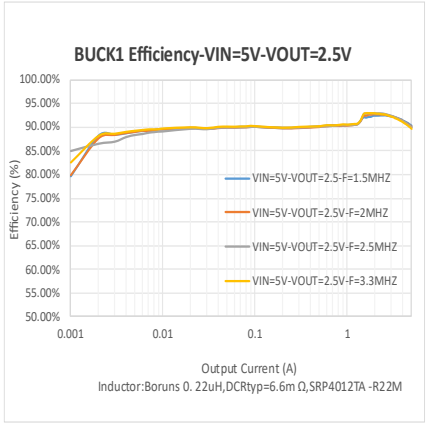


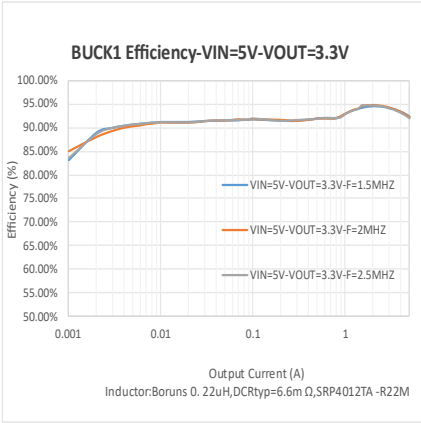


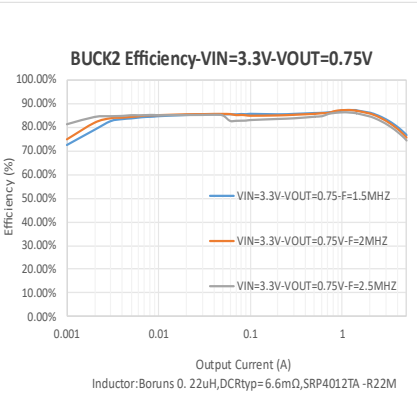


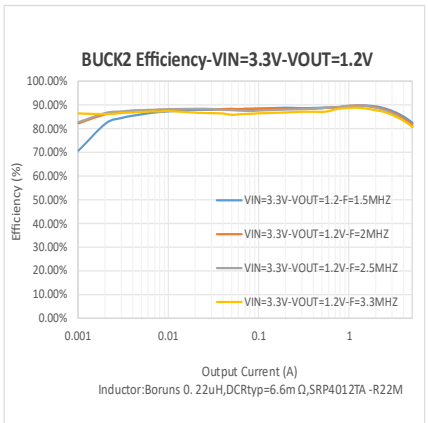
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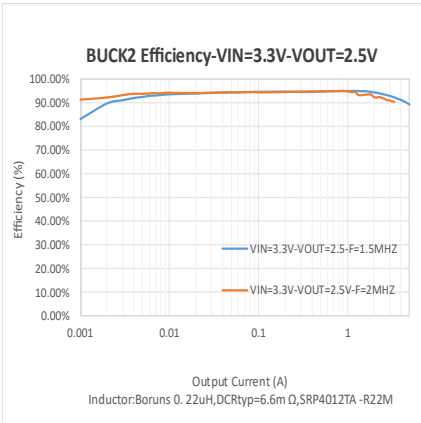


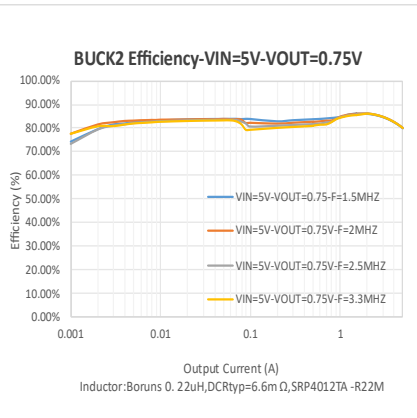


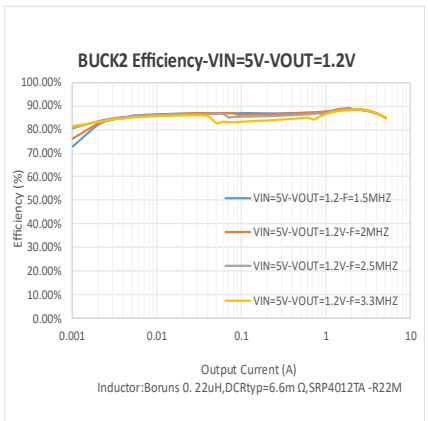


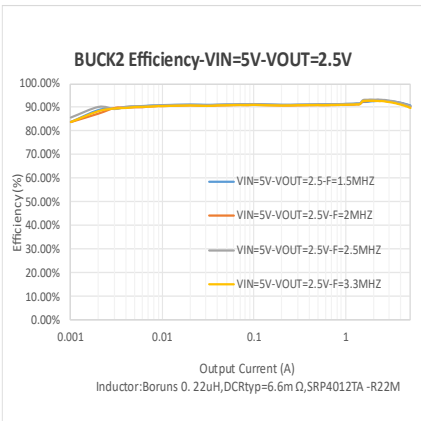




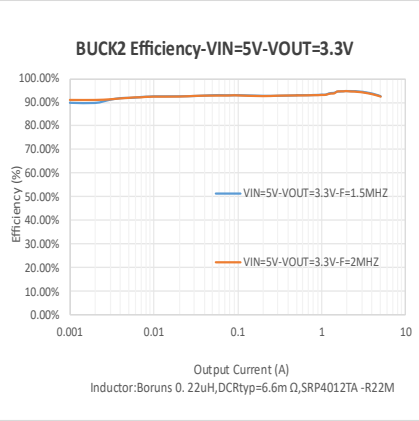


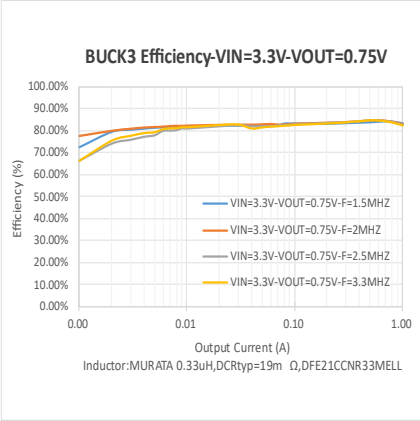


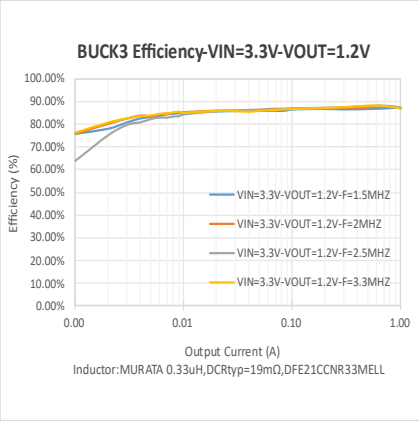


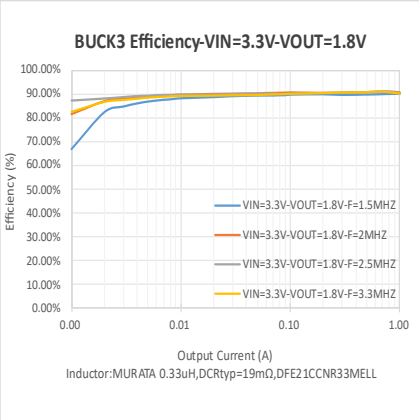


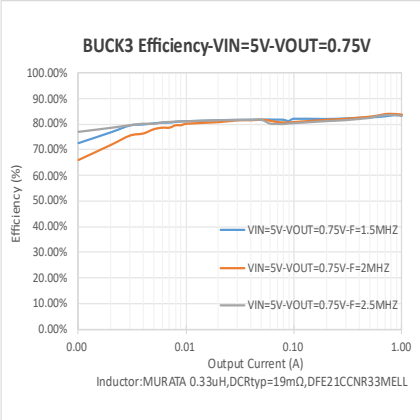
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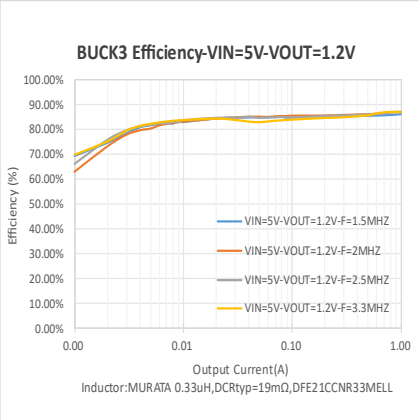


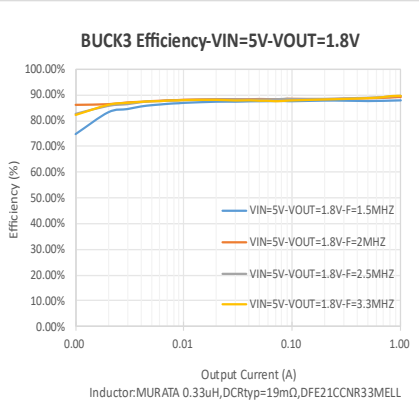


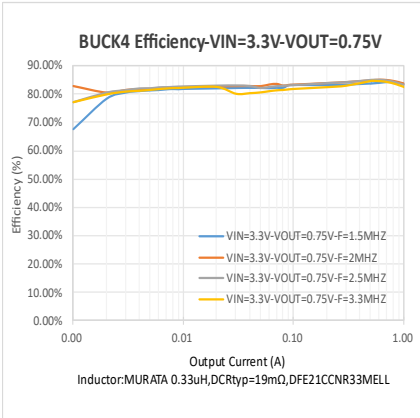


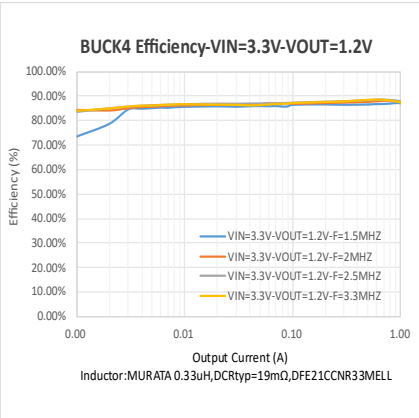




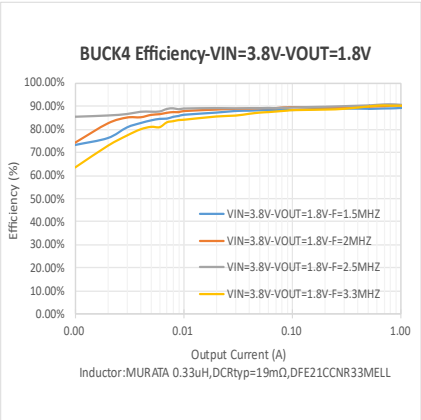


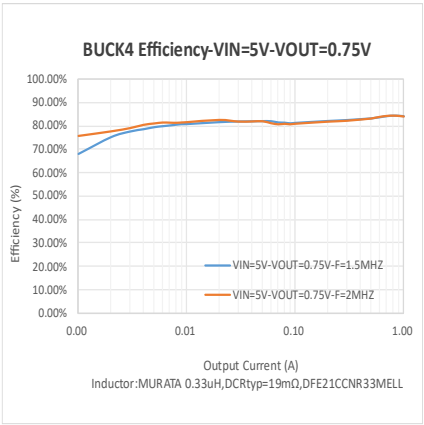


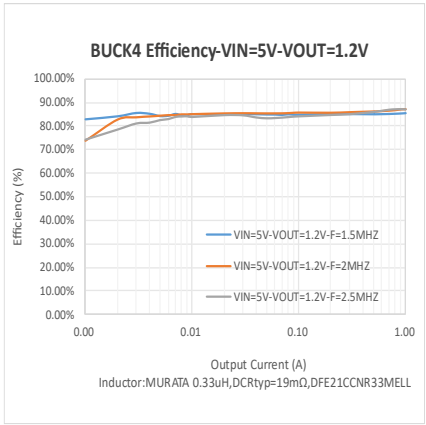


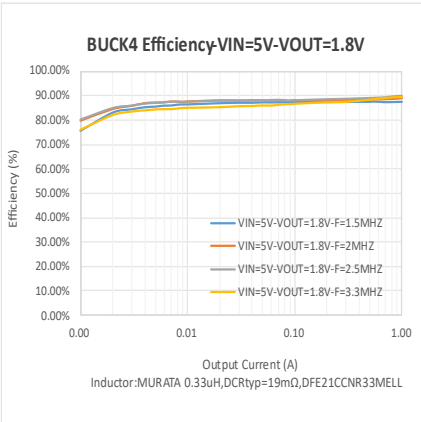


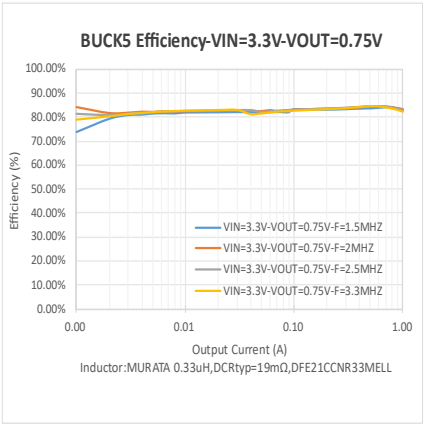
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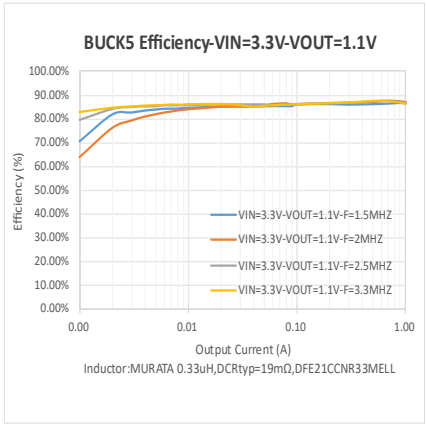


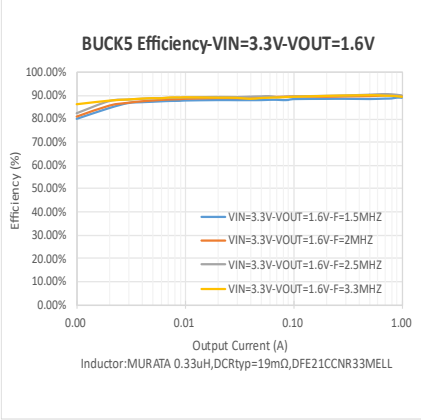


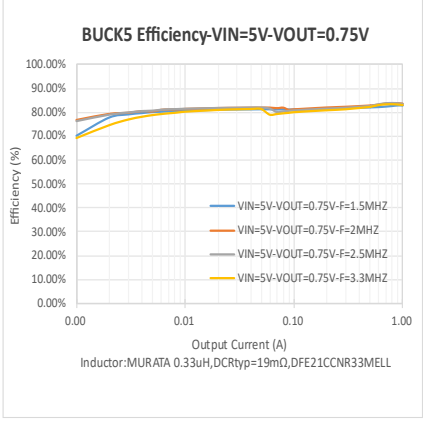


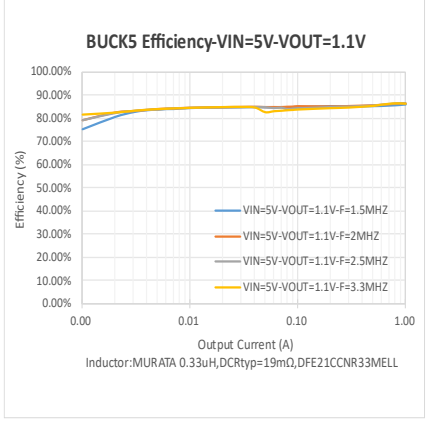




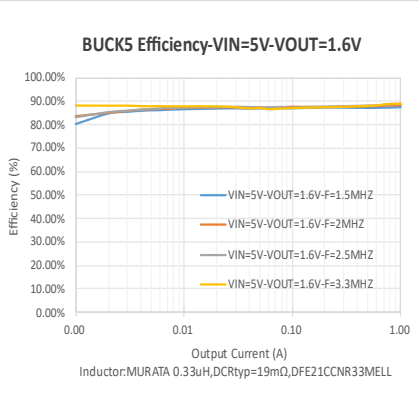


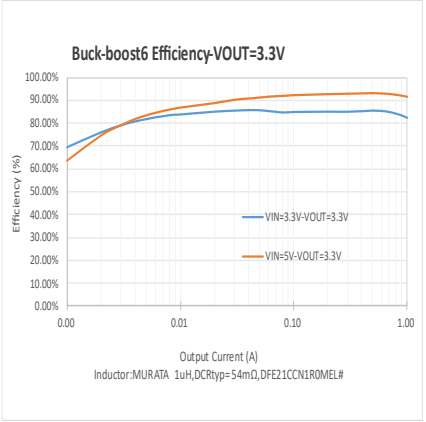


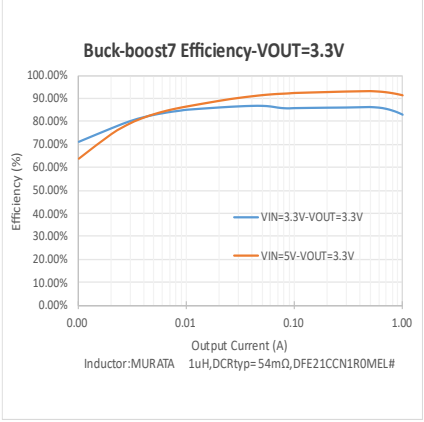




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Schematic

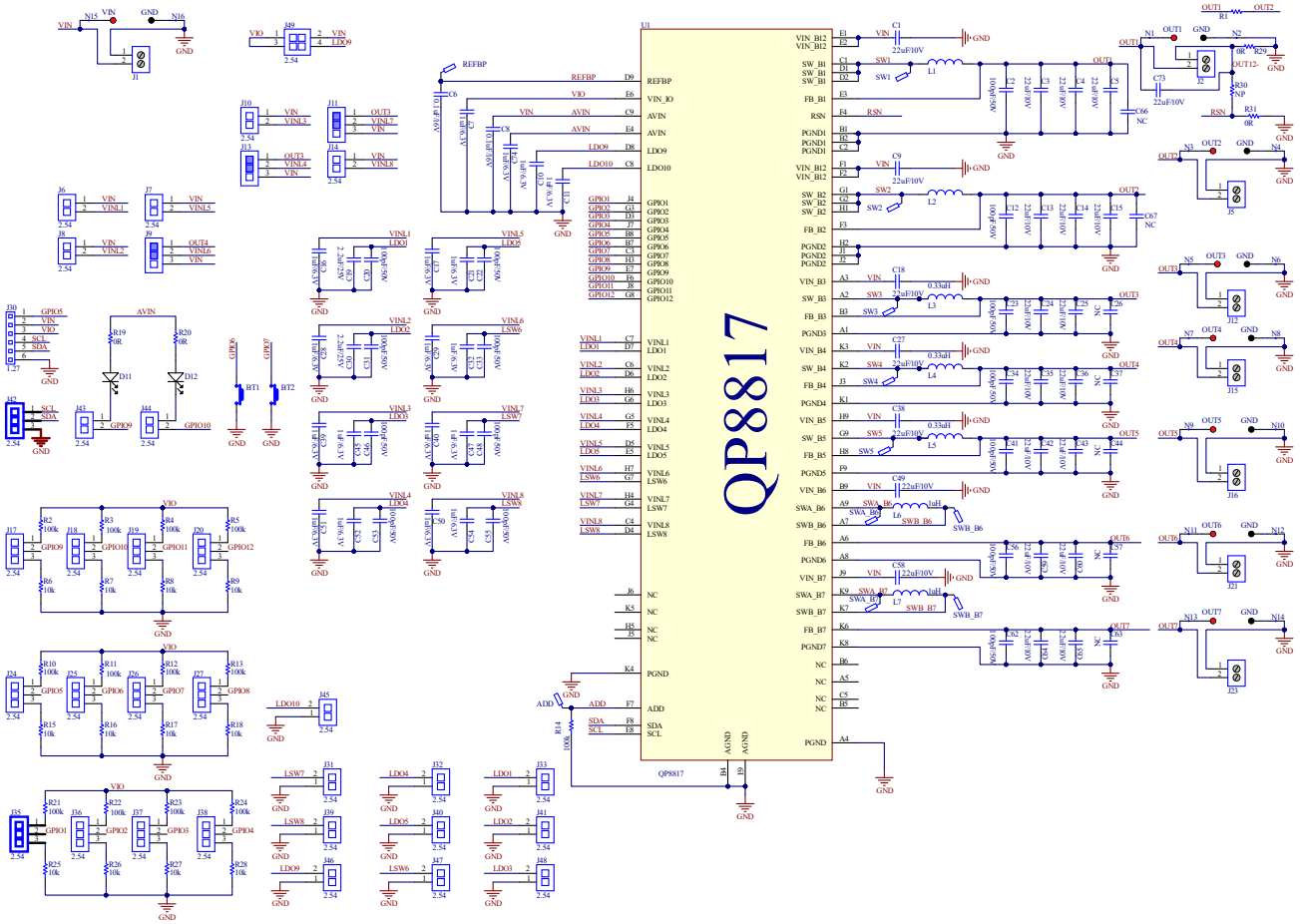


Figure 5 – QP8817AEVB01 Schematic

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Bill of Materials

Table 2 - BOM

Item	Designator	Quantity	Description	Manufacturer	Part Number
1	C1, C3, C4, C5,C9, C13, C14, C15,C18, C24, C25, C27, C35, C36, C38, C42, C43, C49, C58, C59, C60, C64, C65, C73	26	Capacitor, Ceramic,22uF/10V/X7R, 0402	Murata	GRM158R61A226ME15D
2	C2, C12, C20, C22, C23, C31, C33, C34, C41, C46, C48, C53, C55, C56, C62,	15	Capacitor, Ceramic, 100pF/50V, 0201	Murata	GRM033R71H101KA12D
3	C26, C37, C44, C57, C63	0	Capacitor, Ceramic, NC , 0402	Murata	GRM158R61A226ME15D
4	C19,C30	2	Capacitor, Ceramic 2.2uF+/-10% X5R 25V SMD0402	SAMSUNG	CL05A225KA5NUNC
5	C39,C45,C51,C52,C17,C21,C29,C32,C40,C50,C54,C10,C11,C7,C74,C16,C28	18	Capacitor, Ceramic 1uF+/-20% X5R 6.3V SMD0201	Murata	GRM033R60J105MEA2D
6	C6,C8,	2	Capacitor, Ceramic 0.1uF+/-20% X5R 16V SMD0201	Murata	GRM033R61C104KE14D
7	J1, J2, J5, J12, J15, J16, J21, J23	8	Connector, Screw Terminal, 3.50, 2P	Wurth	691214110002S
8	J9, J11, J13,,J17, J18, J19, J20, J24, J25, J26, J27, J35, J36, J37, J38, J42	16	Connector, Jumper, 2.54, 3p	Wurth	61300311121
9	J30	1	Header, Unshrouded , 1.27, Male, 6P	Wurth	GRPB061VWVN-RC
10	J6, J7, J8, J10, J14, J31, J32, J33, J39, J40, J41, J43, J44, J45, J46, J47, J48	17	Header, Unshrouded , 2.54, Male, 2P	Wurth	61300211121
11	J49	1	Header, Unshrouded , 2.54, Male, 2x2P	Wurth	61300421121
12	L1, L2	2	Inductor,0.22uH	Boruns	SRP4012TA-R22M
13	L3, L4, L5,	3	Inductor,0.33uH	MURATA	DFE21CCNR33MELL
14	L6, L7	2	Inductor,1uH	MURATA	DFE21CCN1R0MEL#
15	R19, R20, R31	3	Resistor,0603 0R +/-1%	Standard	Standard
16	R1, R29	2	Resistor,1206 0R +/-1%	Standard	Standard
17	R6, R7, R8, R9, R15, R16, R17, R18, R25, R26, R27, R28	12	Resistor,0603 10kR +/-1%	Standard	Standard
18	R2, R3, R4, R5, R10, R11, R12, R13, R14, R21, R22, R23, R24	13	Resistor,0603 100kR +/-1%	Standard	Standard
19	D11, D12	2	LED, Red/Blue/Green/white , 0805	Standard	Standard
20	R30	0	Resistor,0603 NC	Standard	Standard

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21	BT1, BT2	2	WURTH-434123043836 50mA, 12VDC	WURTH	WURTH-434123043836
22	U1	1	IC, QP8817	Qorvo	QP8817
23	Label	1	QP8817AEVB01,Arial,6	3cm*1cm	
24	TP1(for OUT1), TP3(for OUT2), TP5(for OUT3), TP7(for OUT4), TP9(for OUT5), TP11(for OUT6), TP13(for OUT7), TP15(for VIN),	8	TEST POINT PC MINI .040"D RED	Key-Stone	5000
25	TP2(for GND), TP4(for GND), TP6(for GND), TP8(for GND), TP10(for GND), TP12(for GND), TP14(for GND), TP16(for GND),	8	TEST POINT PC MINI .040"D BLK	Key-Stone	5001

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Layout

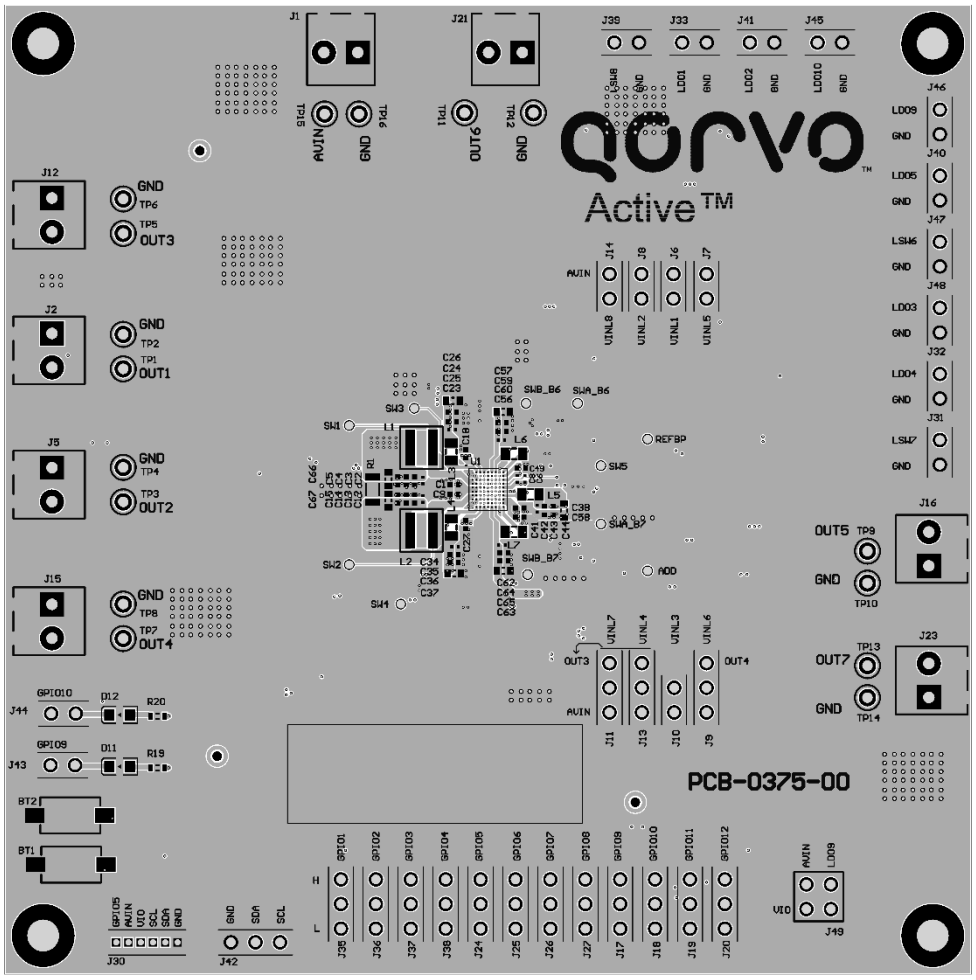


Figure 6 – Layout Top Assembly

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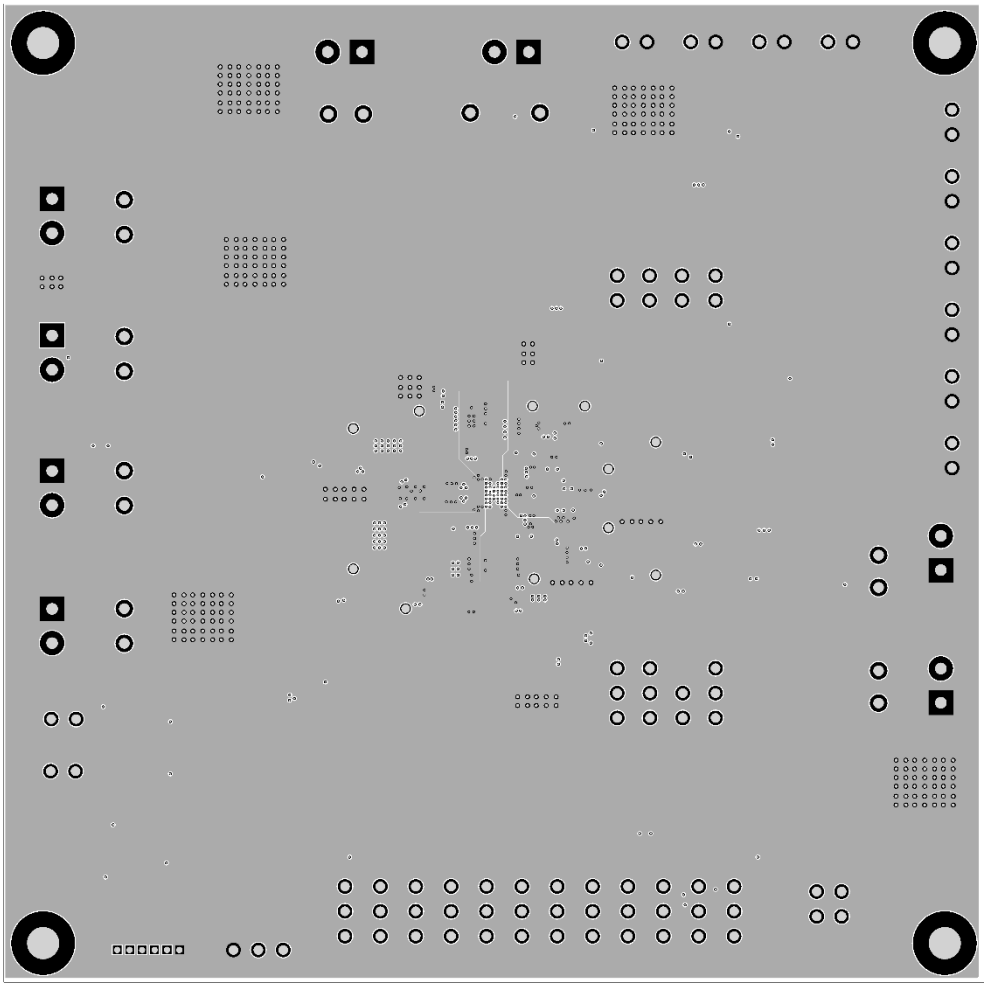


Figure 7 – Layout Sig1 Layer

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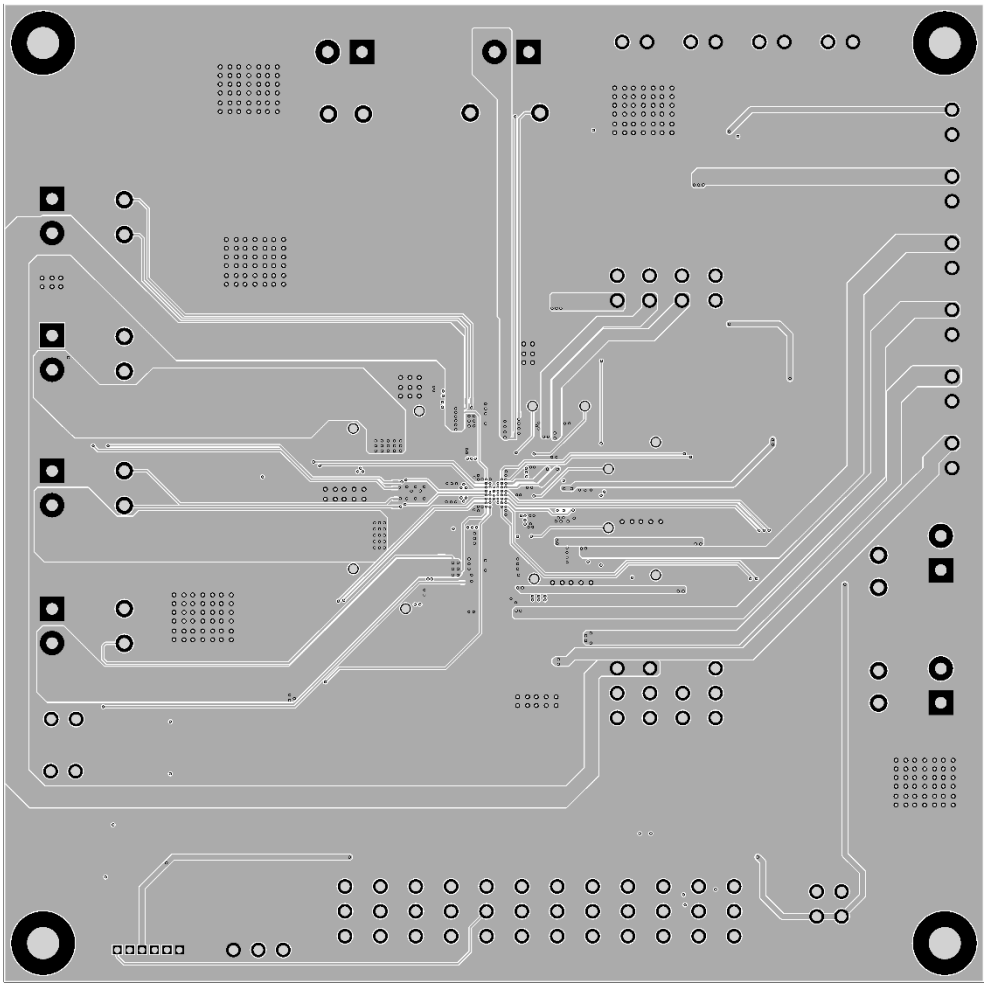


Figure 8 – Layout Sig2 Layer

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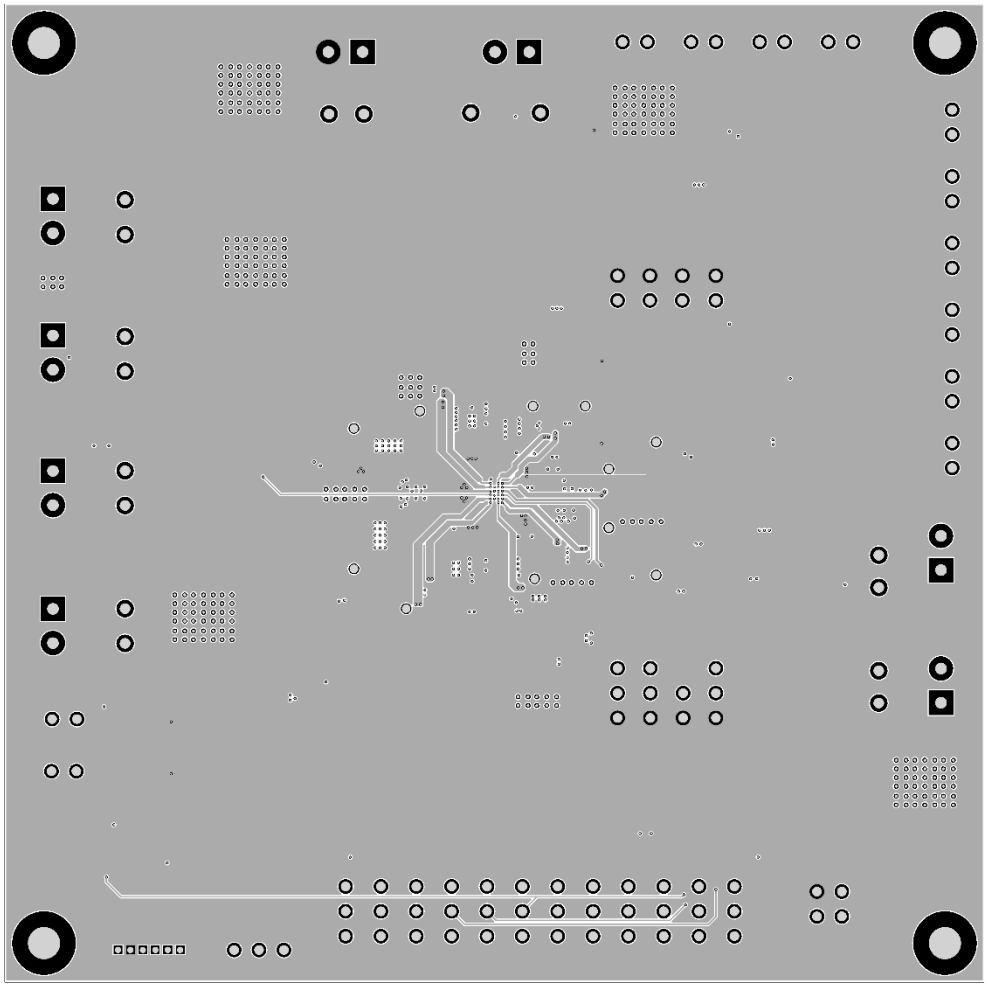


Figure 8.1 – Layout Sig3 Layer

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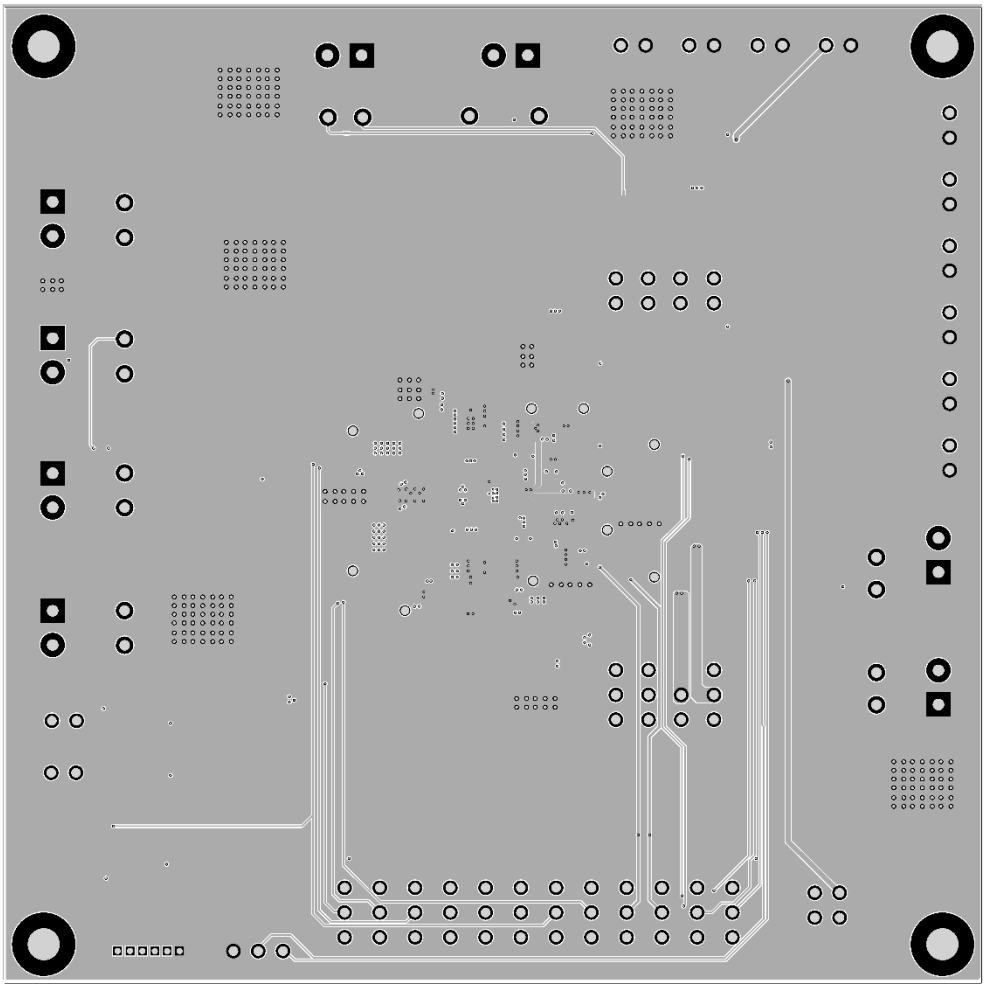


Figure 8.2 – Layout Sig4 Layer

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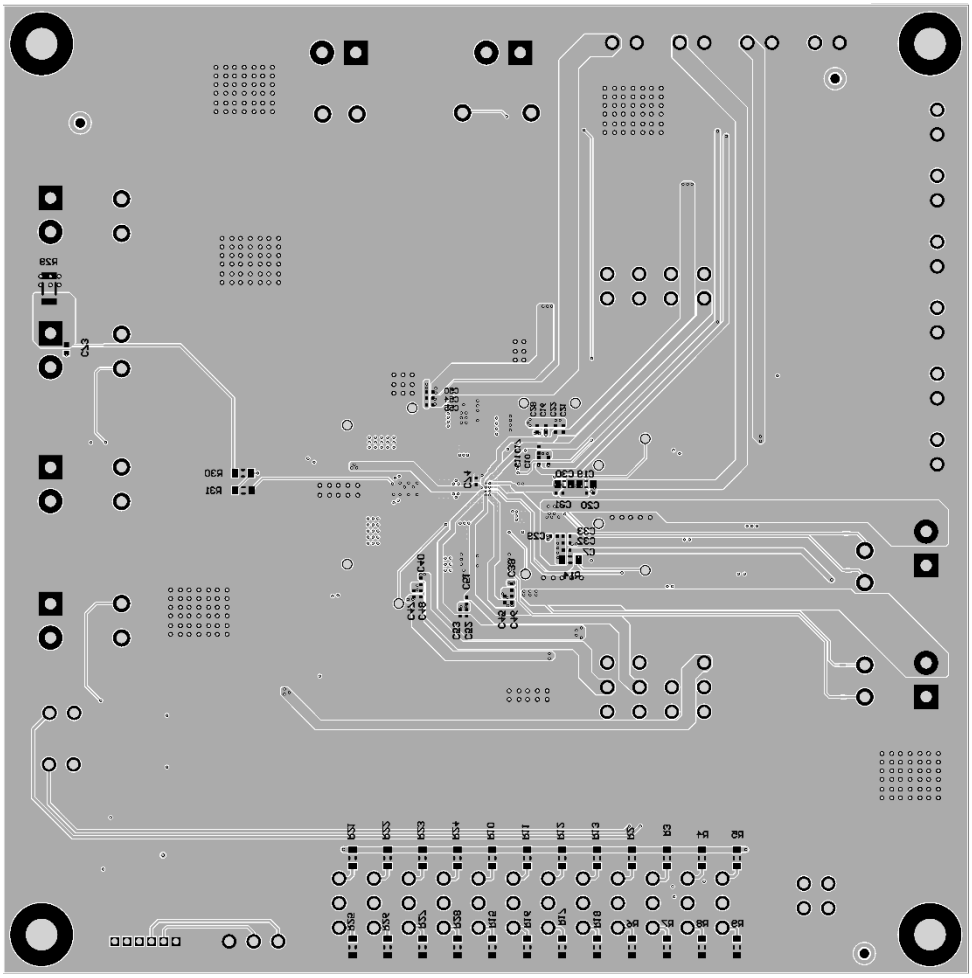


Figure 9 – Layout Bottom Layer

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GUI Installation

1. Download the GUI files from Qorvo's website and save them on your computer.
2. Plug the USB-TO-I2C dongle into a free USB port.
3. Double click on the QP8817 GUI.exe to start the QP8817 GUI.

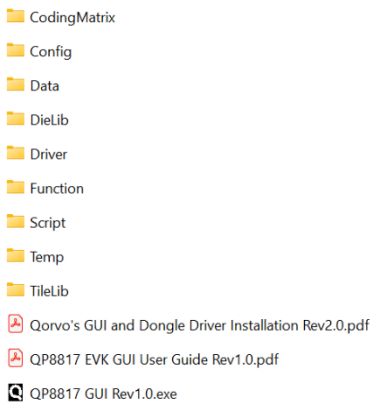


Figure 10 – Dongle Driver

GUI Overview

The GUI has 2 basic function buttons allocated in top-left of the Tool Bar which are Read and Write I2C. The GUI contains 2 setting modes: Configuration Mode and Regulator Mode. In Configuration Mode screen it displays each regulator's basic information on a single page, all the information is user configurable via the drop-down boxes. Regulator Mode allows the user to view and change the IC's advanced internal registers of each regulator.

Configuration Mode

Click the "Configuration" button in the left of the GUI screen to see the basic user programmable options. This display mode allows user to change some basic settings of each regulator (voltages, current…). User can use either the mouse scroll or the right-side scroll bar to navigate to other regulators. Using drop-boxes, left-click the small arrow next to the value, then a selection pop-up displays all possible options to choose from. Scroll up/down to find the target value and left-click to select it. After the required parameters are changed, click the "Write" button to transfer the changes from the GUI to the IC.

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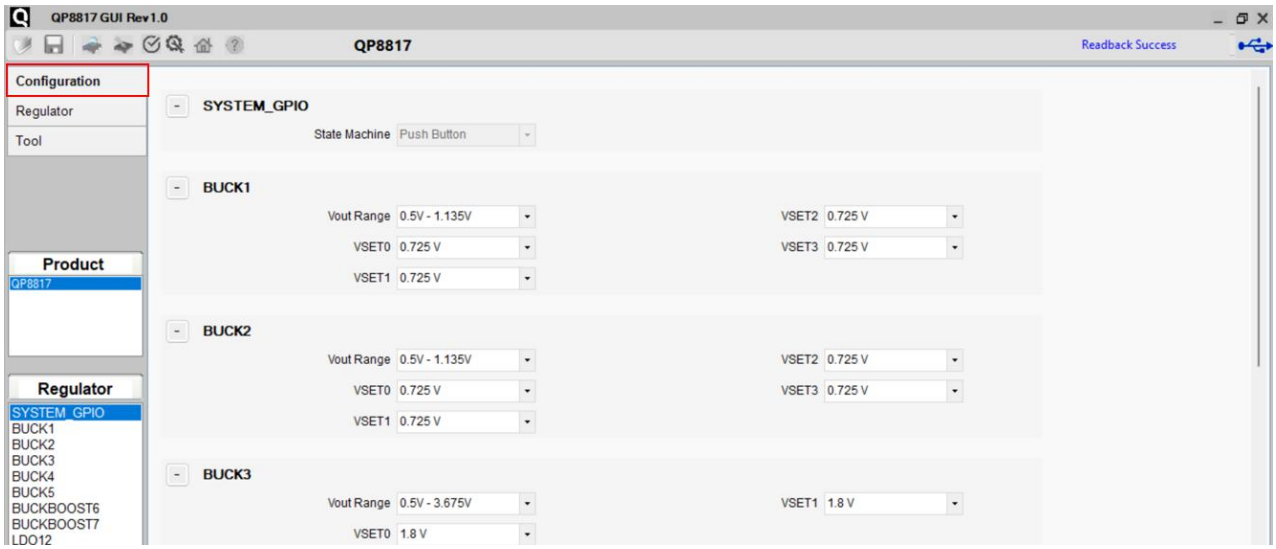


Figure 11 – GUI Configuration Mode

Regulator Mode

Click the “Regulator” button in the left of the GUI screen to see all available user programmable options. In the left side of the screen, click on the Tiles Selector to choose which regulator or LDO’s to show. There are two tabs for each tile, “Setting “and “Register”.

The “Settings” tab is easy to read and has drop down menus that show the available choices. The “Registers” tab shows the actual register values required to achieve a desired setting.

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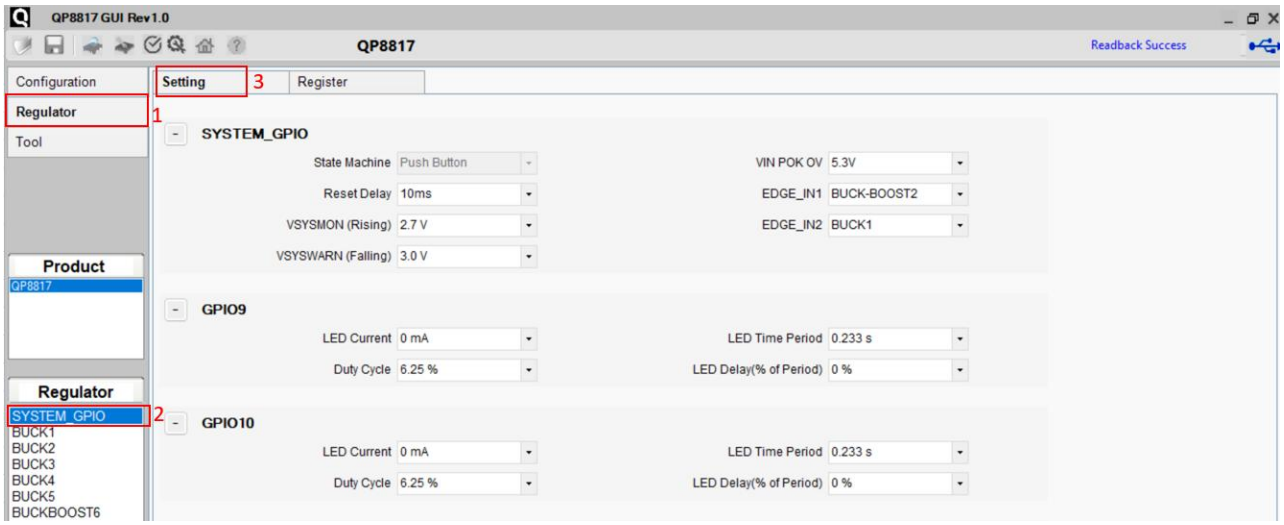


Figure 12 – GUI Setting Tab of Regulator Mode

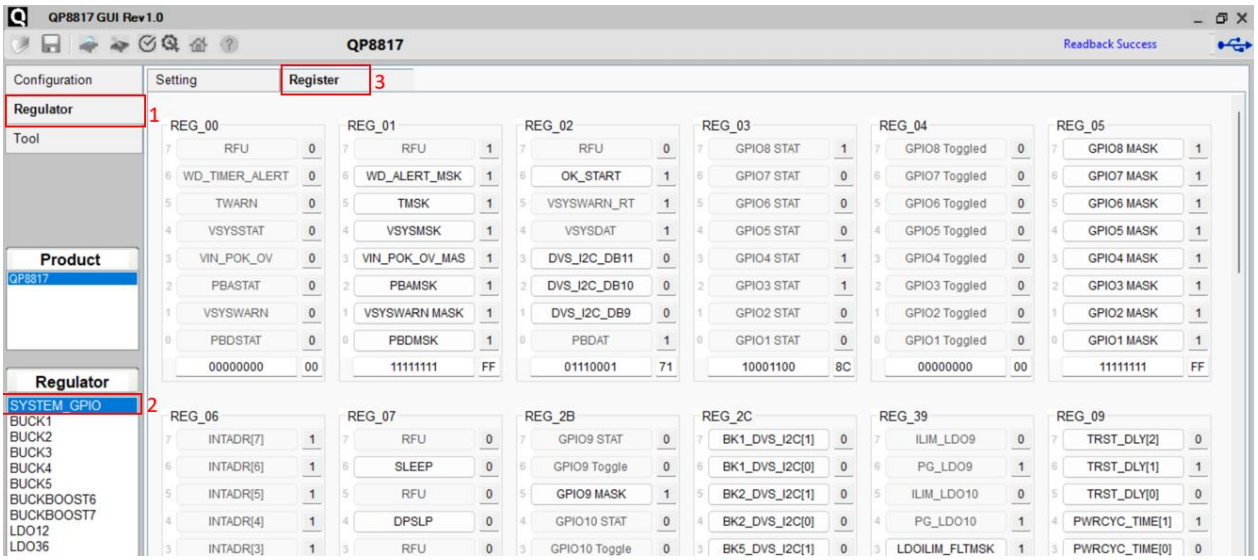


Figure 13 – GUI Register Tab of Regulator Mode

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Button Descriptions

Read: Clicking on this button reads the QP8817 registers and displays them in the GUI. Note that this reads all registers. Active-Semi recommends reading registers each time the QP8817 powers-up to acquire the initial register settings. Qorvo also recommends reading registers after making changes to them. Immediately reading the registers after a write confirms the changes were properly stored. This also updates the SYSTEM STATUS box to ensure that one of the changes did not generate a fault condition.

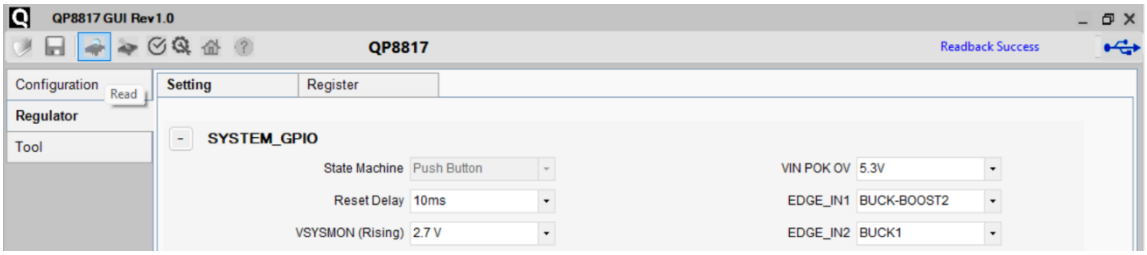


Figure 14 – Read Button

Write: Clicking on this button writes the GUI settings to the QP8817's registers. All registers are written, regardless of whether or not they were changed.

Note: Remember that changes to the GUI settings are not transferred into the IC until the GUI's "Write" button is pressed.

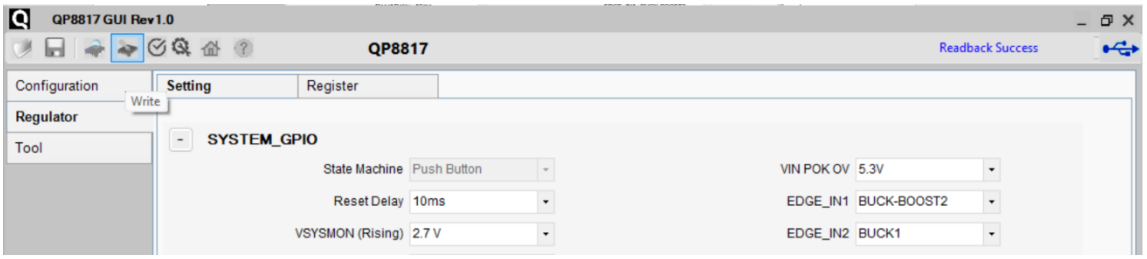


Figure 15 – Write Button

Confirm: Confirm function allows user to compare the QP8817's registers information on GUI with the values read from IC.

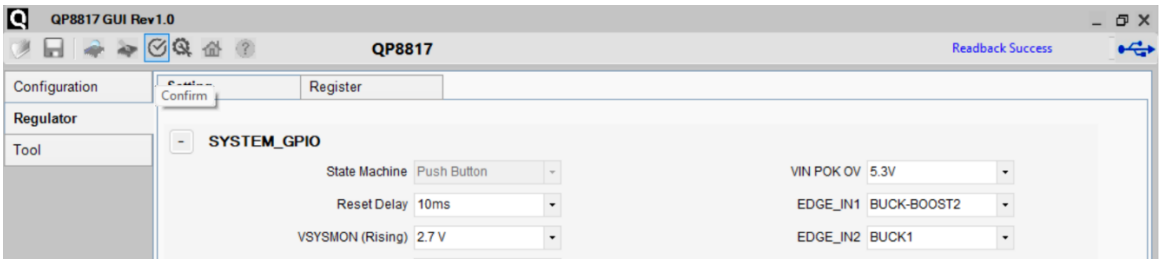


Figure 16 – Confirm Button

I2C Configuration: I2C configuration function allows user to select one of the QP8817's I2C addresses for Read/Write function.

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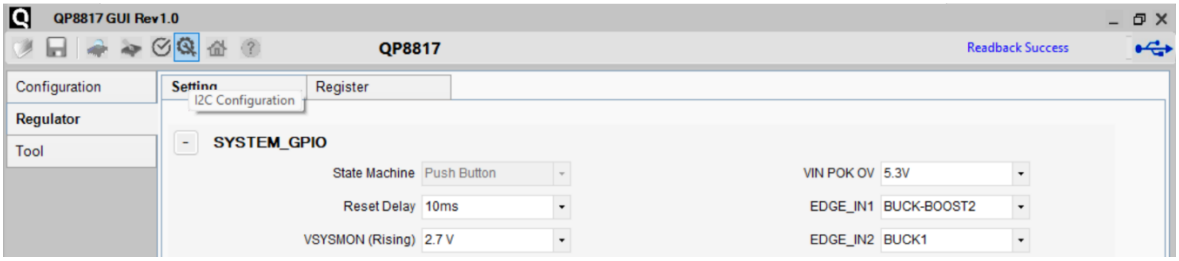


Figure 17 – I2C Configuration Button

Dongle Connection Status: The GUI also contains a dongle is connected status which indicates that Active-Semi's USB-TO-I2C dongle is connected to the USB port of the driver installed. The figure below shows the two possible indication status graphics.



Figure 18 – Dongle Connection Status

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